

# Compal Confidential

## BAUS0 BAUY0 DIS M/B Schematics Document

AMD Stoney SOC with DDR4

AMD R16M-M1-30

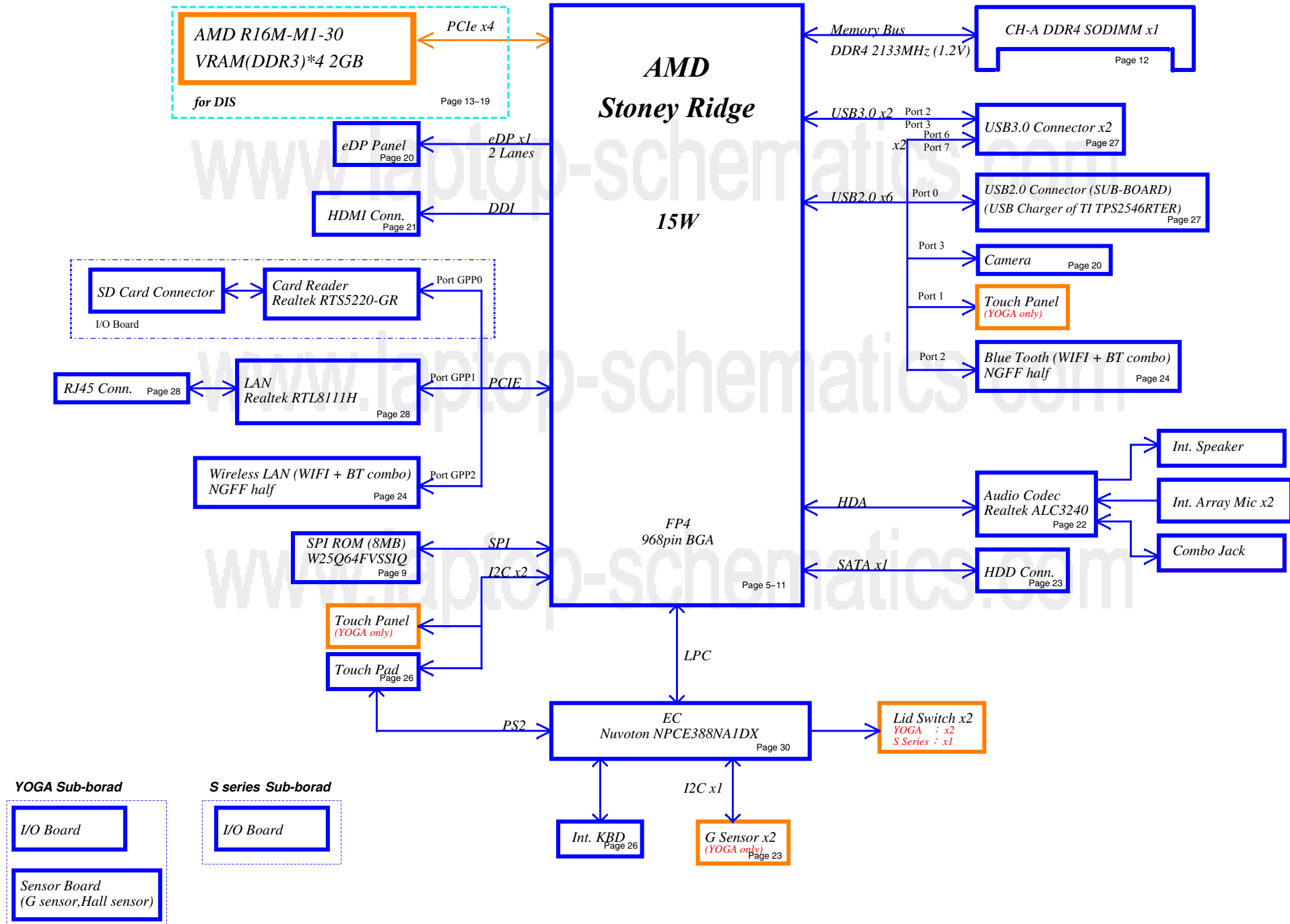
2015-12-17

LA-D541P

REV : 0.2

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# YOGA/S series



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Voltage Rails

Power Plane	Description	S0	S3	S5
VIN	Adapter power supply (20V)	ON	ON	ON
B+	AC or battery power rail for power circuit.	ON	ON	ON
+APU_CORE	Core voltage for APU	ON	OFF	OFF
+APU_CORE_NB	Voltage for On-die VGA of APU	ON	OFF	OFF
+VGA_CORE	0.95-1.2V switched power rail	ON	OFF	OFF
+3VALW	3.3V always on power rail	ON	ON	ON
+3VS	3.3V switched power rail	ON	OFF	OFF
+1.8VALW	1.8V always on power rail	ON	ON	ON
+1.8VS	1.8V switched power rail	ON	OFF	OFF
+0.95VALW	0.95V always on power rail	ON	ON	ON
+0.95VS	0.95V switched power rail	ON	OFF	OFF
+1.2V	1.2V power rail for APU and DDR	ON	ON	OFF
+2.5V	2.5V power rail for DDR	ON	ON	OFF
+1.5VS	1.5V switched power rail	ON	OFF	OFF
+3VGS	3.3V switched power rail for VGA	ON	OFF	OFF
+1.8VGS	1.8V switched power rail for VGA	ON	OFF	OFF
+1.5VGS	1.5V switched power rail for VGA	ON	OFF	OFF
+0.95VGS	0.95V switched power rail for VGA	ON	OFF	OFF
+5VALW	5V always on power rail	ON	ON	ON
+5VS	5V switched power rail	ON	OFF	OFF
+RTC_APU	RTC power	ON	ON	ON
+0.6VS	0.6V switched power rail for DDR terminator	ON	OFF	OFF
+0.775VALW*	0.775V always on power rail	OFF*	ON*	ON*

SMBUS Control Table

	SOURCE	VGA	BATT	EC	SODIMM	WLAN	Thermal Sensor	APU	CRT RTD2168
EC_SMB_CK1 EC_SMB_DA1	388N +3VALW	X	V +3VALW	V +3VALW	X	X	X	X	X
APU_SCLK0 APU_SDATA0	APU +3VS	X	X	X	V +3VS	X	X	X	X
EC_SMB_CK2 EC_SMB_DA2	388N +3VS	V +3VGS	X	V +3VALW	X	X	V +3VGS	V +1.8VS (+3VS)	V +3VS

EC SM Bus1 address			EC SM Bus2 address		
Device	Address	HEX	Device	Address	HEX
Smart Battery	0001 011X b	16H	Thermal Sensor	1001 101X b	9AH
Charger	0001 0010 b	12H	SB-TSI (APU)	1001 100X b	98H
			VGA Internal Thermal	1000 001X b	82H

APU  
SM Bus address

Device	Address	HEX
DDR DIMM1	1010 001Xb	A2H

Stoney\_APU

UAPU1 A9@  
SA00009PT00  
S IC ZM2901FY23AC 2.9G BGA 968P

UAPU1 A6@  
SA00009PU00  
S IC ZM2401AVY23AC 2.5G BGA 968P

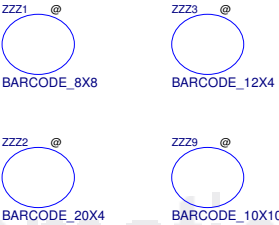
UAPU1 E2@  
SA00009PV00  
S IC ZM2001AVY23AC 2.1G BGA 968P

PCB

ZZZ DA6001JJ000  
PCB 1NZ LA-D541P REV0 M/B  
SS@

ZZZ DA6001JJ100  
PCB 100 LA-D541P REV0 M/B  
YOGA@

BARCODE



APU PCIE PORT LIST

Port	Device
GPP0	Card Reader
GPP1	LAN
GPP2	WLAN
GPP3	

USB Port Table

USB 2.0	USB 3.0	Port	3 External USB Port
		0	USB2.0 (Charger)
		1	Touch Screen
		2	WLAN/BT Combo
		3	Camera
		4	
		5	
	XHCI	2	USB3.0
		3	USB3.0

STATE	SIGNAL	SLP_S3#	SLP_S5#	+VALW	+V	+VS	Clock
Full ON		HIGH	HIGH	ON	ON	ON	ON
S3 (Suspend to RAM)		HIGH	HIGH	ON	ON	OFF	OFF
S4 (Suspend to Disk)		LOW	HIGH	ON	OFF	OFF	OFF
S5 (Soft OFF)		LOW	LOW	ON	OFF	OFF	OFF

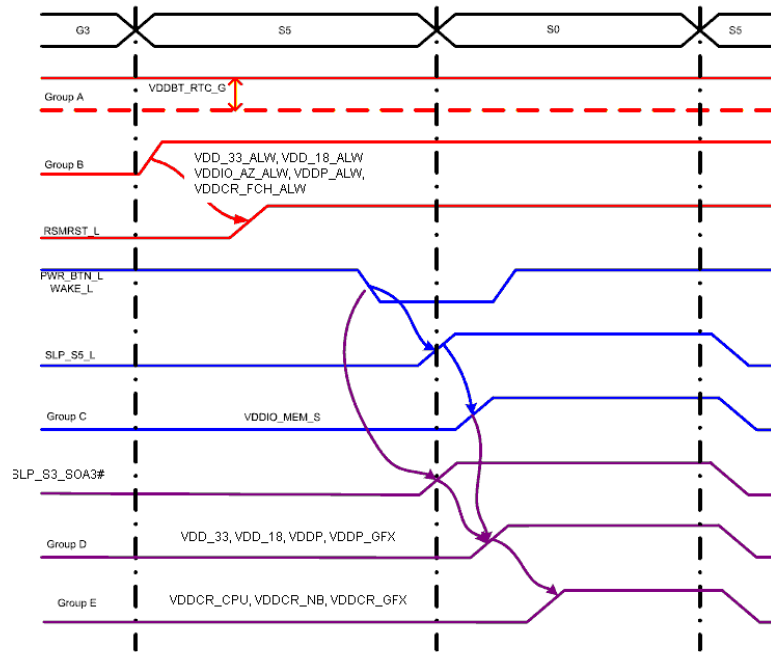
USB OC MAPPING

OC#	USB Port	
0	USB20 port0	
1	USB20 port6,7	USB30 port2,3
2		
3		

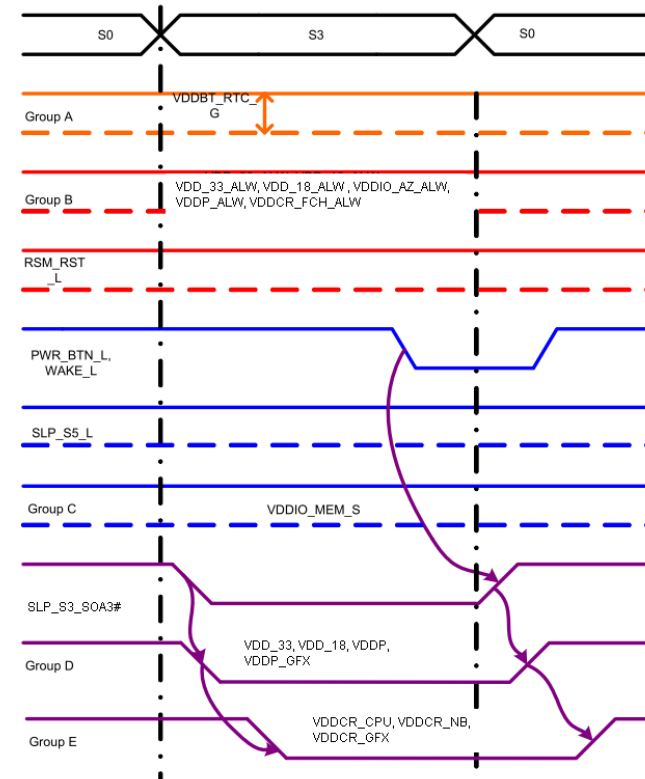
BOM Structure Table

BOM Structure	Item
45@	For HDMI Logo
UMA@	For UMA
DIS@	For DIS
EMI@	EMI pop component
@EMI@	EMI Unpop component
ESD@	ESD pop component
@ESD@	ESD Unpop component
RFS@	S-series RF component
RFY@	YOGA RF component
ME@	ME part
EX_THM@	For thermal sensor component
KBL@	Keyboard backlight
NOKBL@	Non-Keybaord backlight
X76@	X76 part
A9@	A9 CPU
A6@	A6 CPU
E2@	E2 CPU
SS@	For S series
YOGA@	For YOGA
8111H@	For 8111H
GIGA@	For GIGA LAN
100@	For 100 LAN
@	Unpop
H2G@	For HYNIX 2G VRAM
S2G@	For SAMSUNG 2G VRAM
M2G@	For MICRON 2G VRAM
RF@	RF pop component
@RF@	RF Unpop component

Power up Sequencing G3 TO S0



Power up Sequencing S0 TO S3 TO S0



Power on Sequence required:

1. There is no sequencing requirement between power supplies within the individual power groups.
2. All power supplies in Group A must be stable and within specifications for 5 seconds before any power supply in Power sequencing Group B is greater than 10% of its specified minimum operating voltage.
3. All power supplies in Group B must be stable and within specifications before any power supply in Power sequencing Group C is greater than 10% of its specified minimum operating voltage.
4. All power supplies in Group C must be stable and within specifications before any power supply in Power sequencing Group D is greater than 10% of its specified minimum operating voltage.

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DP2 is not support on FP4 Type 2 processors.

HDMI  
eDP

Place resistor(0ohm) for SVT in PWR side

<38> APU SVT  
<38> APU\_SVC  
<38> APU\_SVD

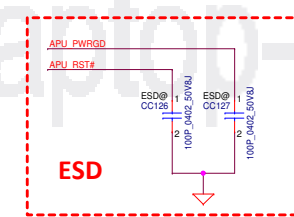
SVCI, SVD1, SVT1  
Not support in Type3.

<38> APU\_PWRGD  
<38> H\_PROCHOT#

APU SVT  
APU SVC R  
APU SVD R  
APU SVCI  
APU SID  
APU ALERT#  
APU TDI  
APU TDO  
APU TCK  
APU TMS  
APU TRST#  
APU DBRDY  
APU DBREQ#

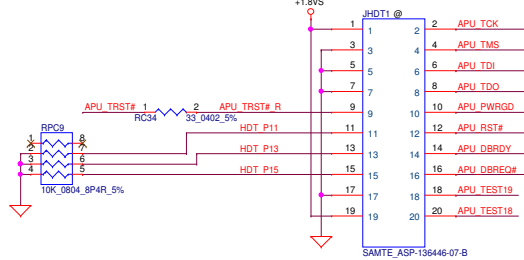
EC SMB CK2  
EC SMB DA2

APU SVCI  
APU SID  
APU TDI  
APU TDO  
APU TCK  
APU TMS  
APU TRST#  
APU DBRDY  
APU DBREQ#

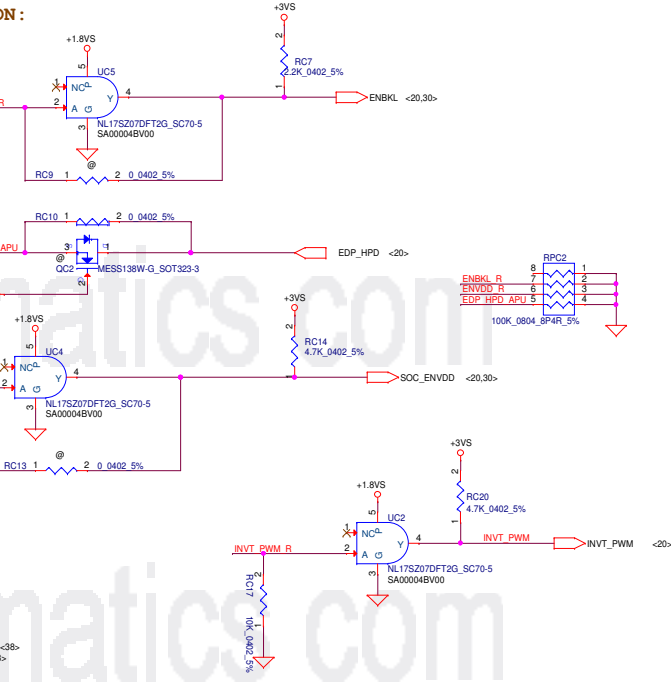


ESD

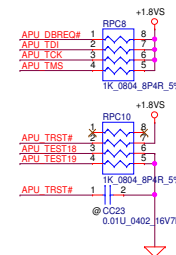
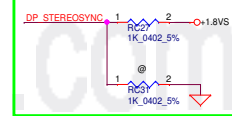
HDT+



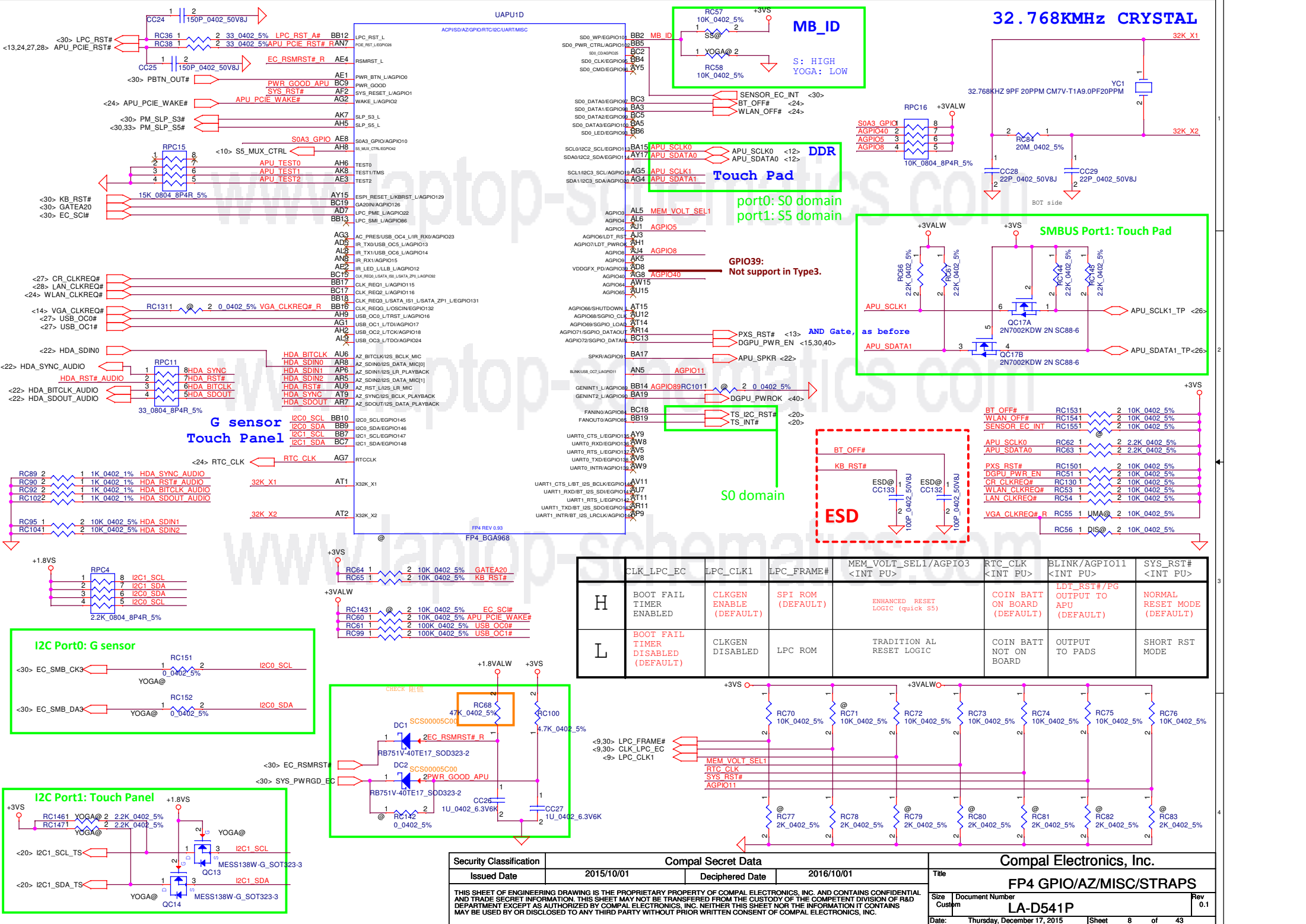
DP\_VARY\_BL, DP\_BLON, DP\_DIGN:  
Type1&3--> VDD\_18  
Type2--> VDD\_33



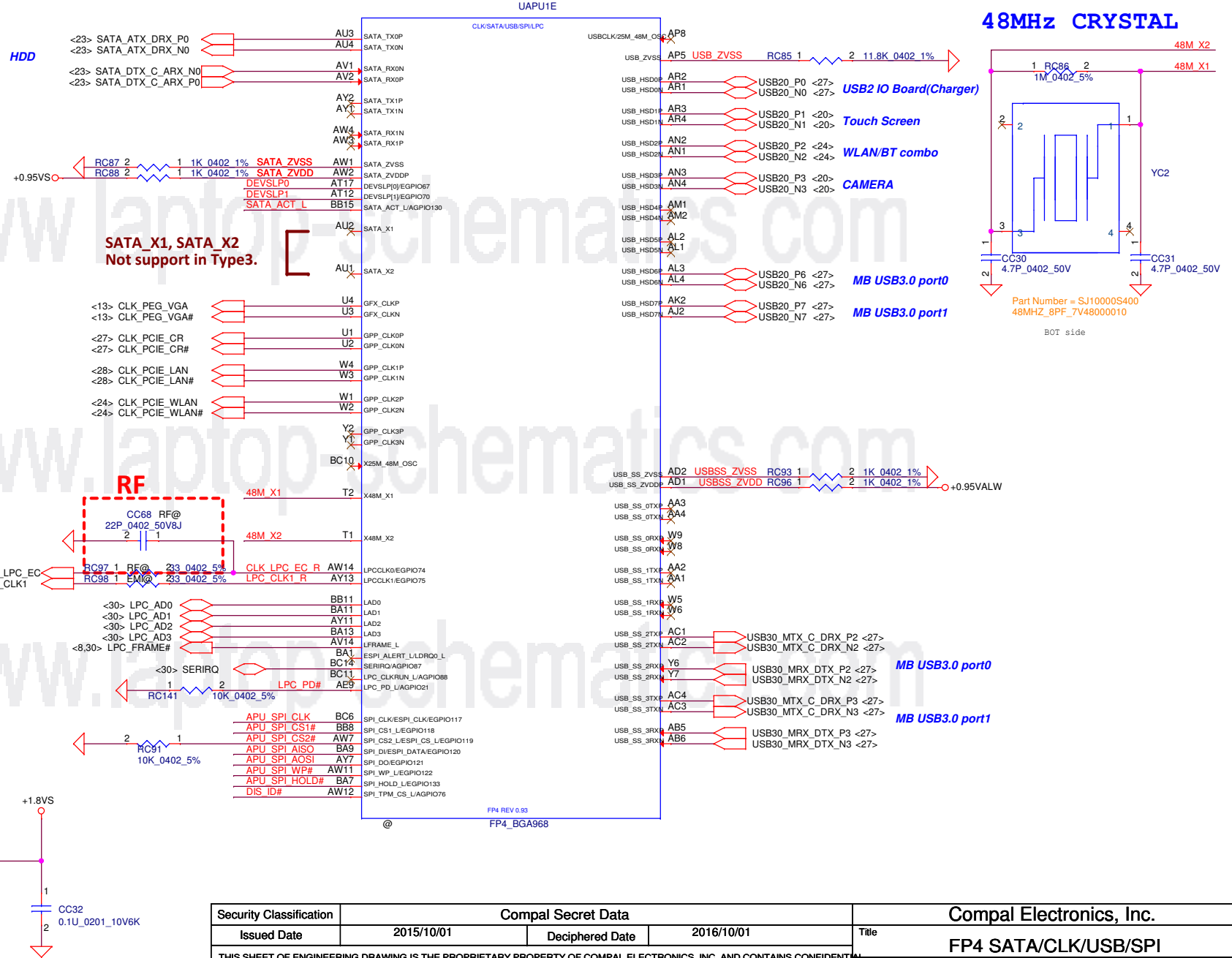
DP\_STEREOSYNC:  
checklist-->PD  
check with AMD-->PU



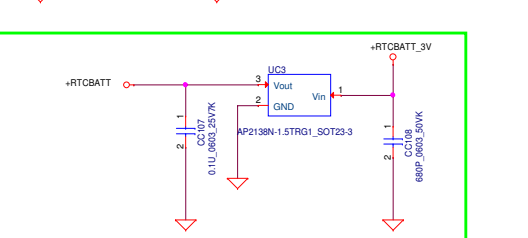
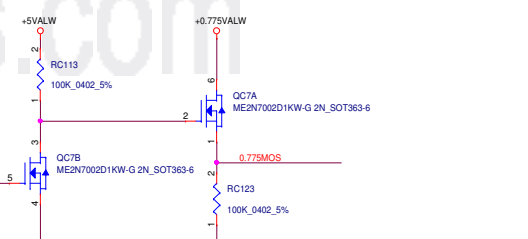
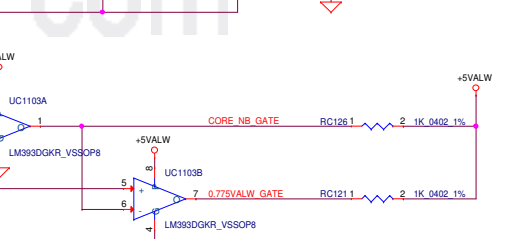
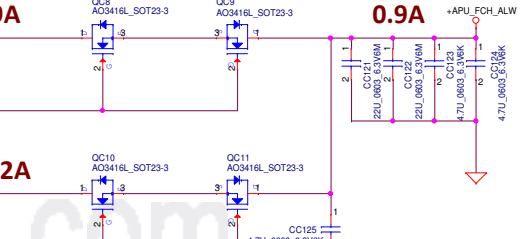
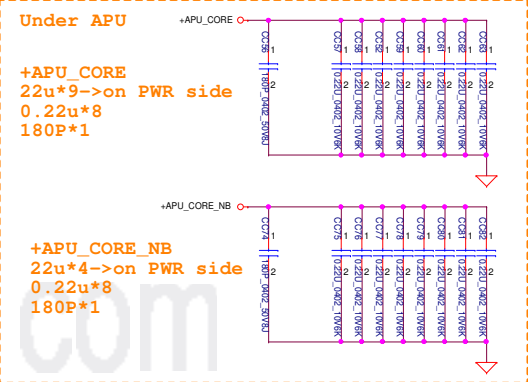
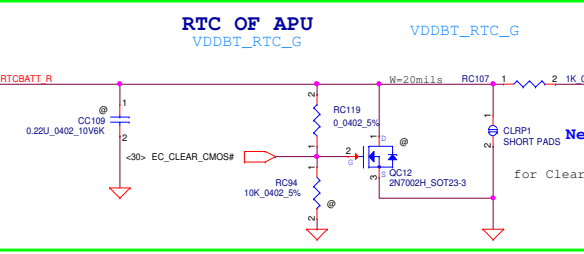
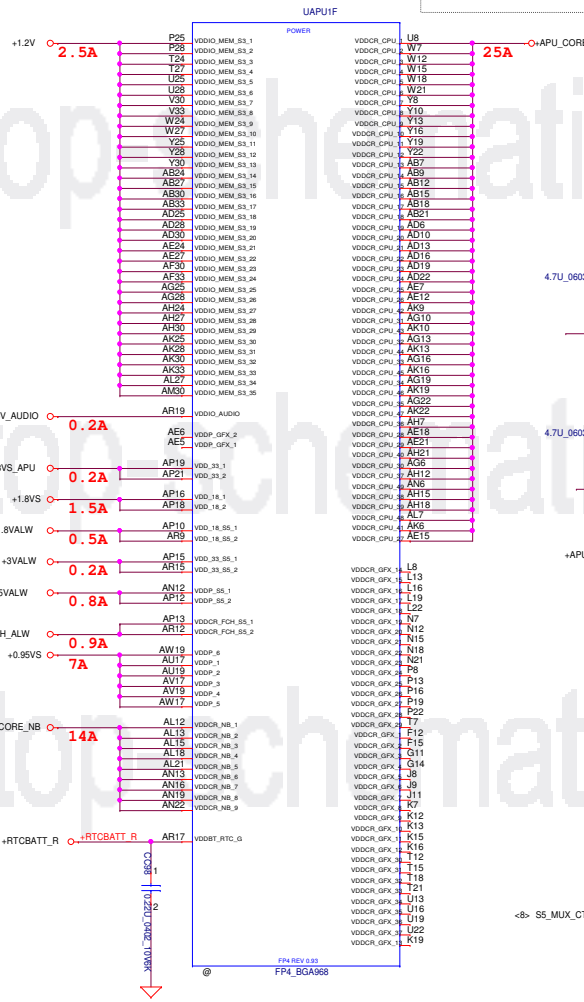
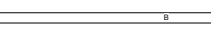
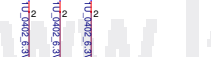
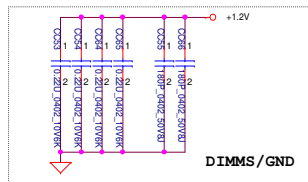
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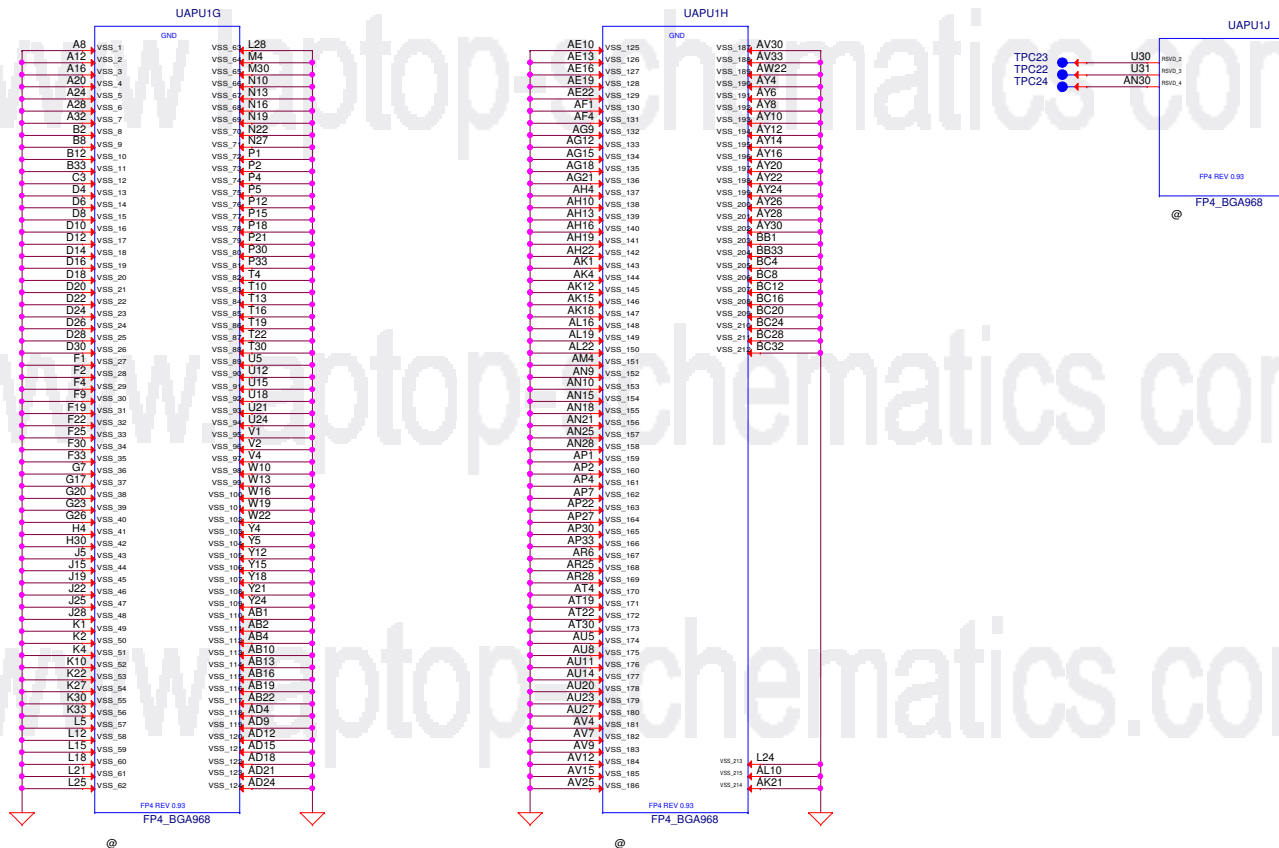




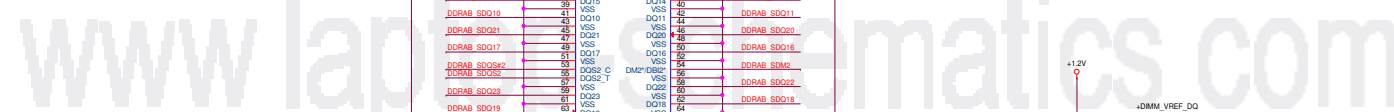
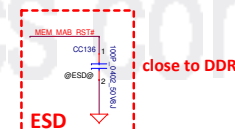
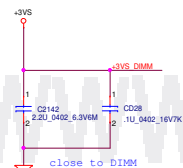
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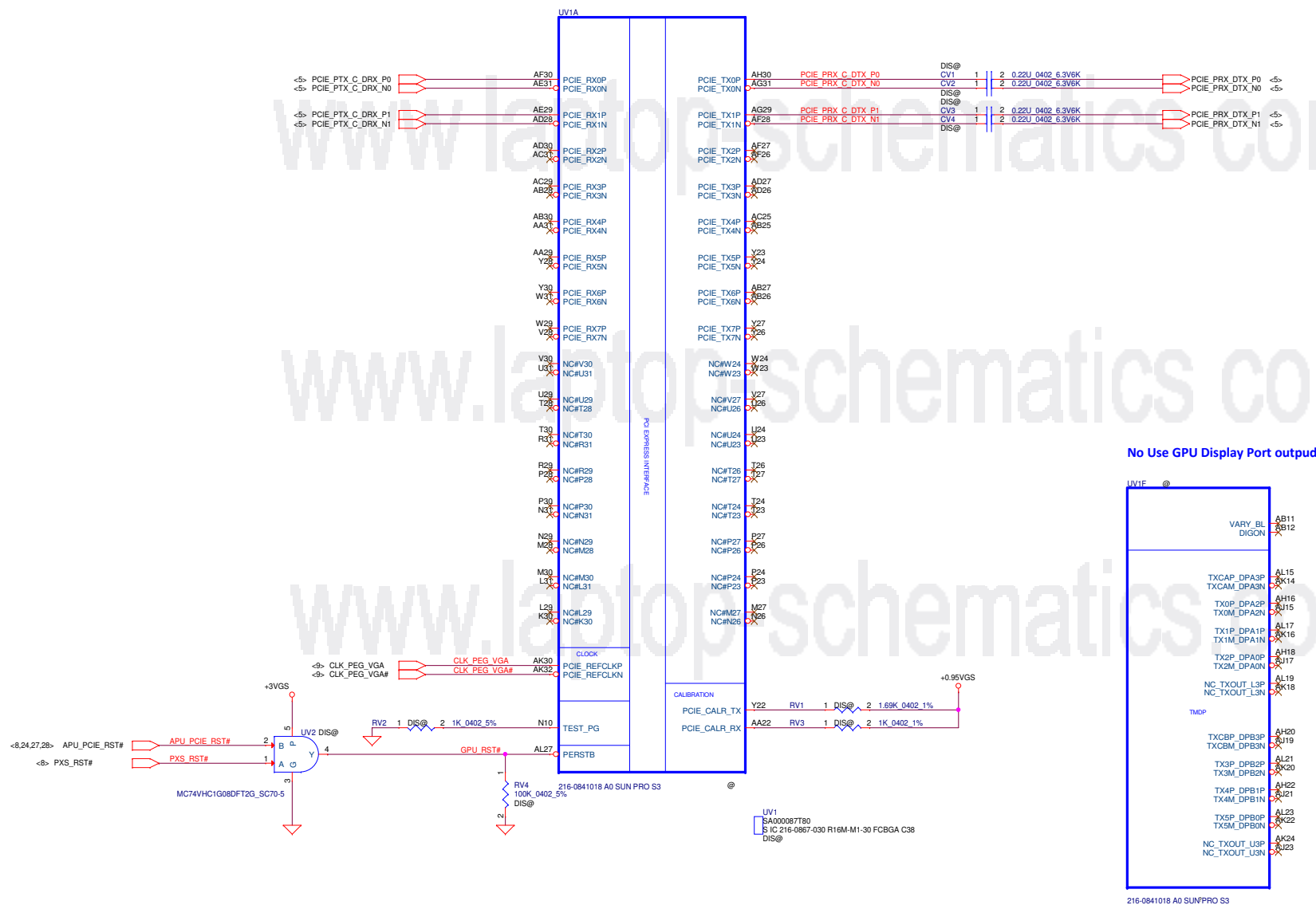
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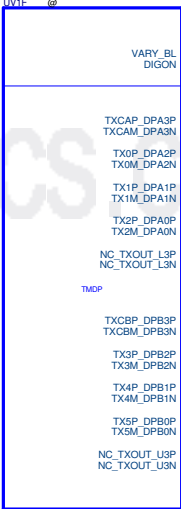
Security Classification		Compal Secret Data		Compal Electronics, Inc.			
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				Doc Number	<b>LA-5511</b>	
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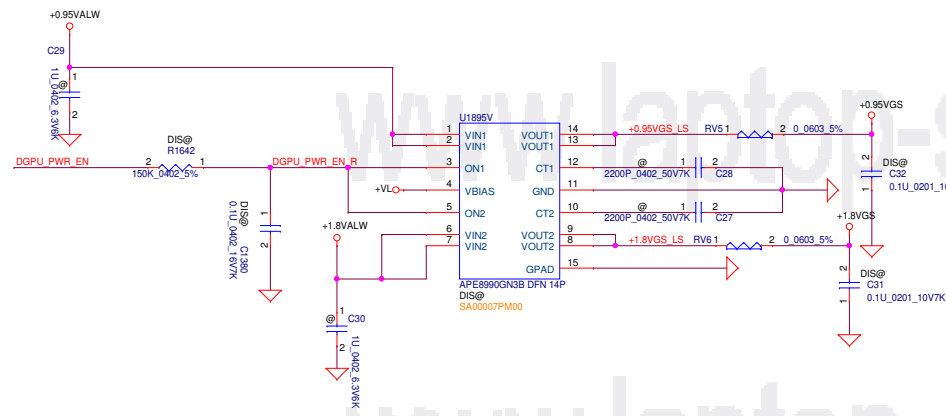


No Use GPU Display Port output

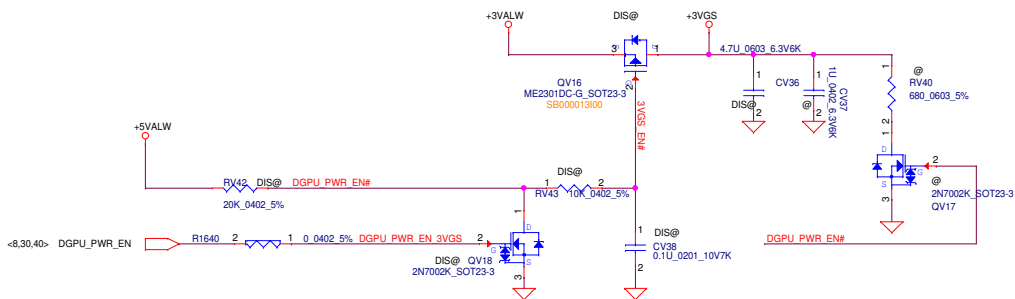




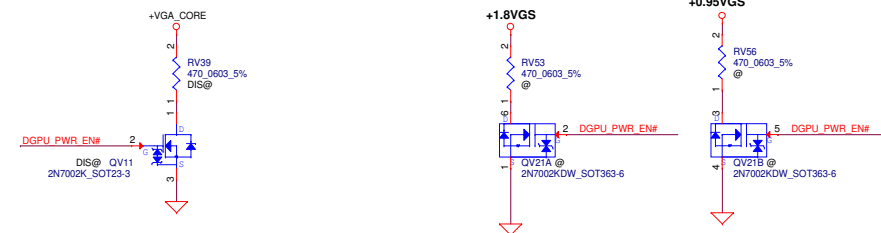
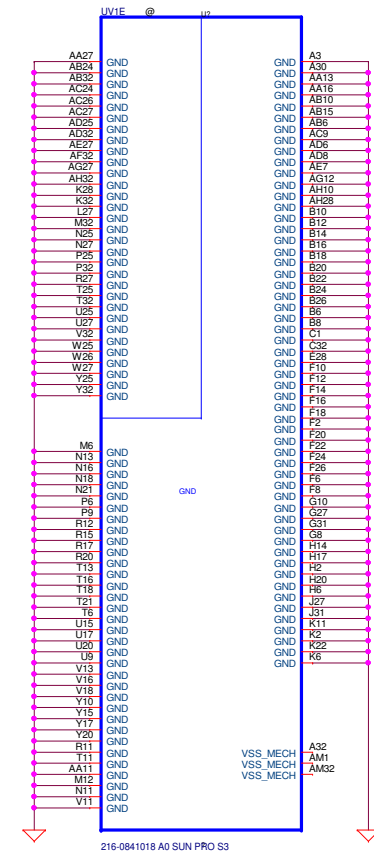
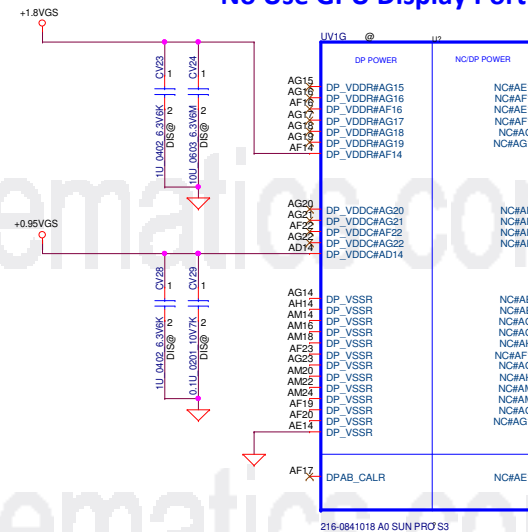
**+1.8VALW TO +1.8VGS**  
**+0.95VALW TO +0.95VGS**  
**Load switch**



**+3VS to +3VGS**

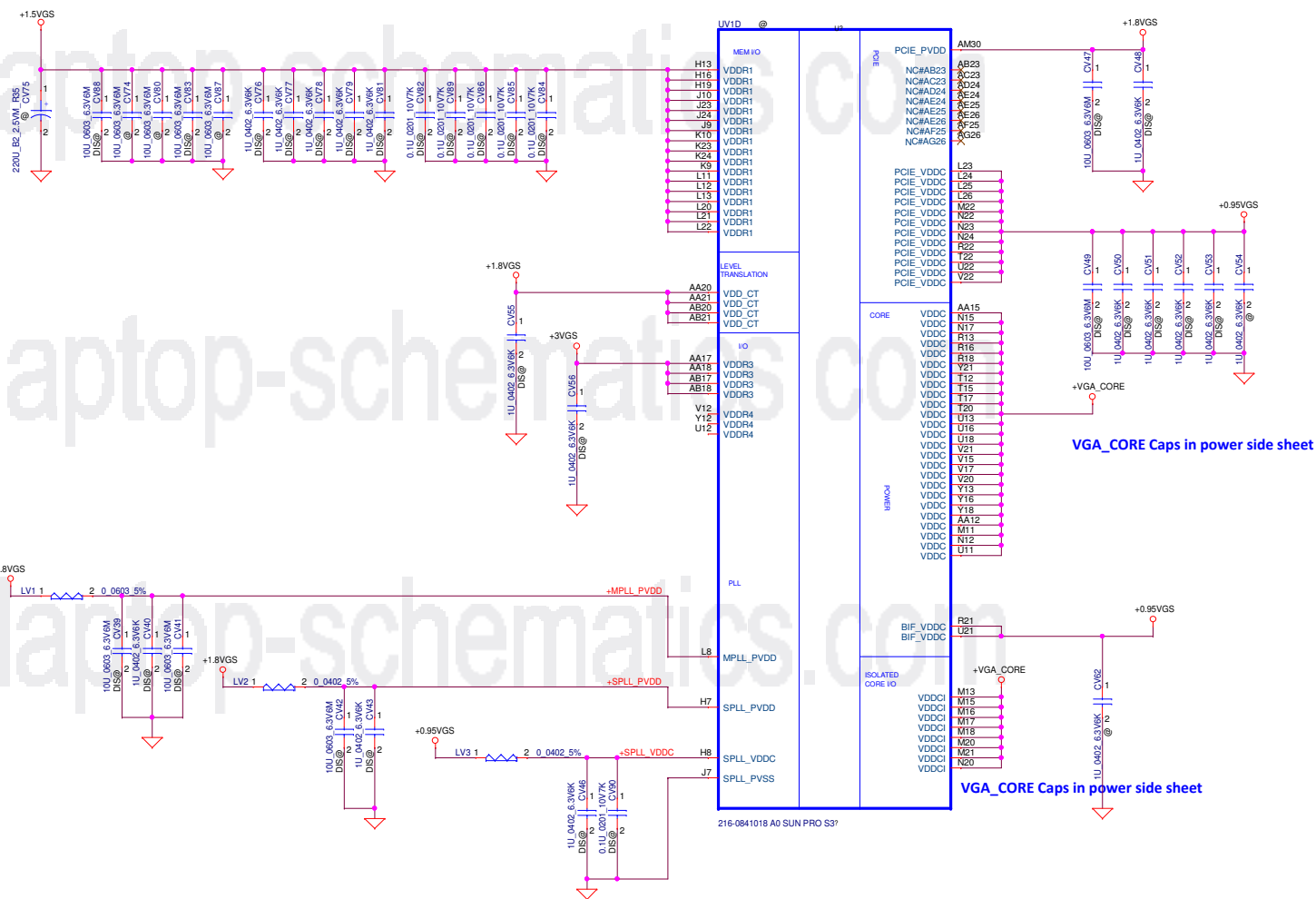


## No Use GPU Display Port output



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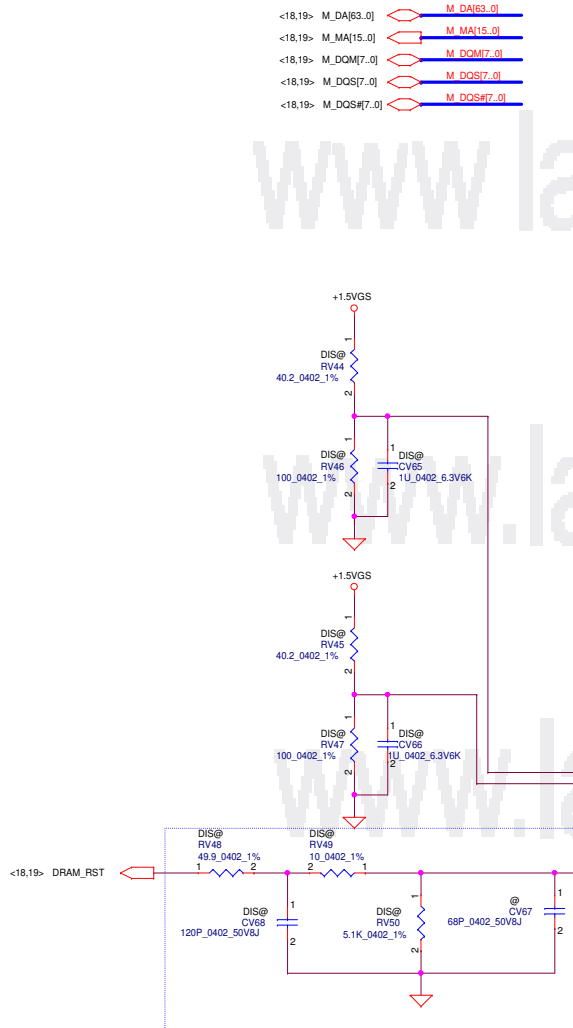


VGA\_CORE Caps in power side sheet

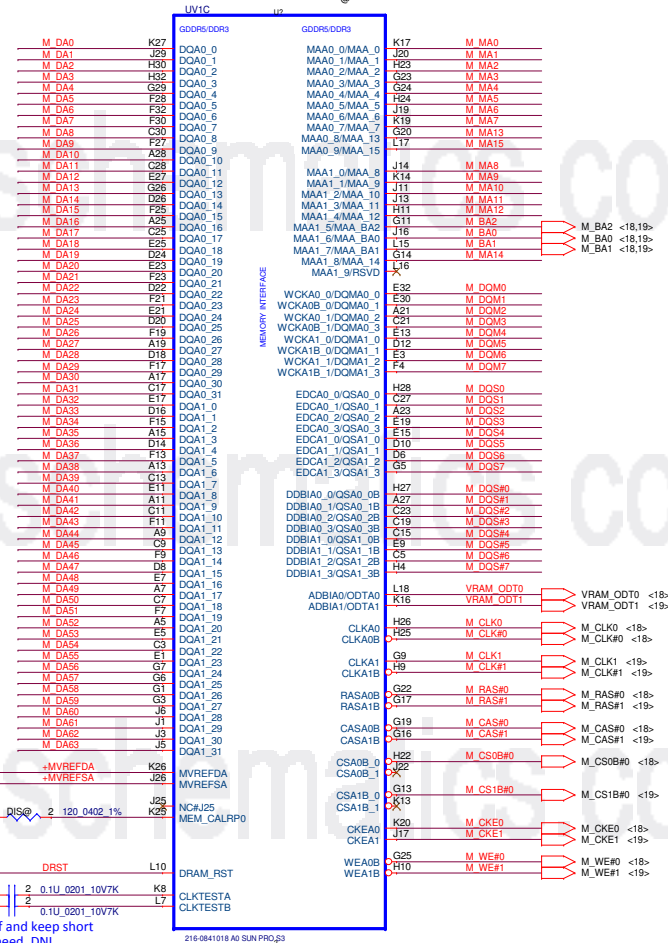
VGA\_CORE Caps in power side sheet

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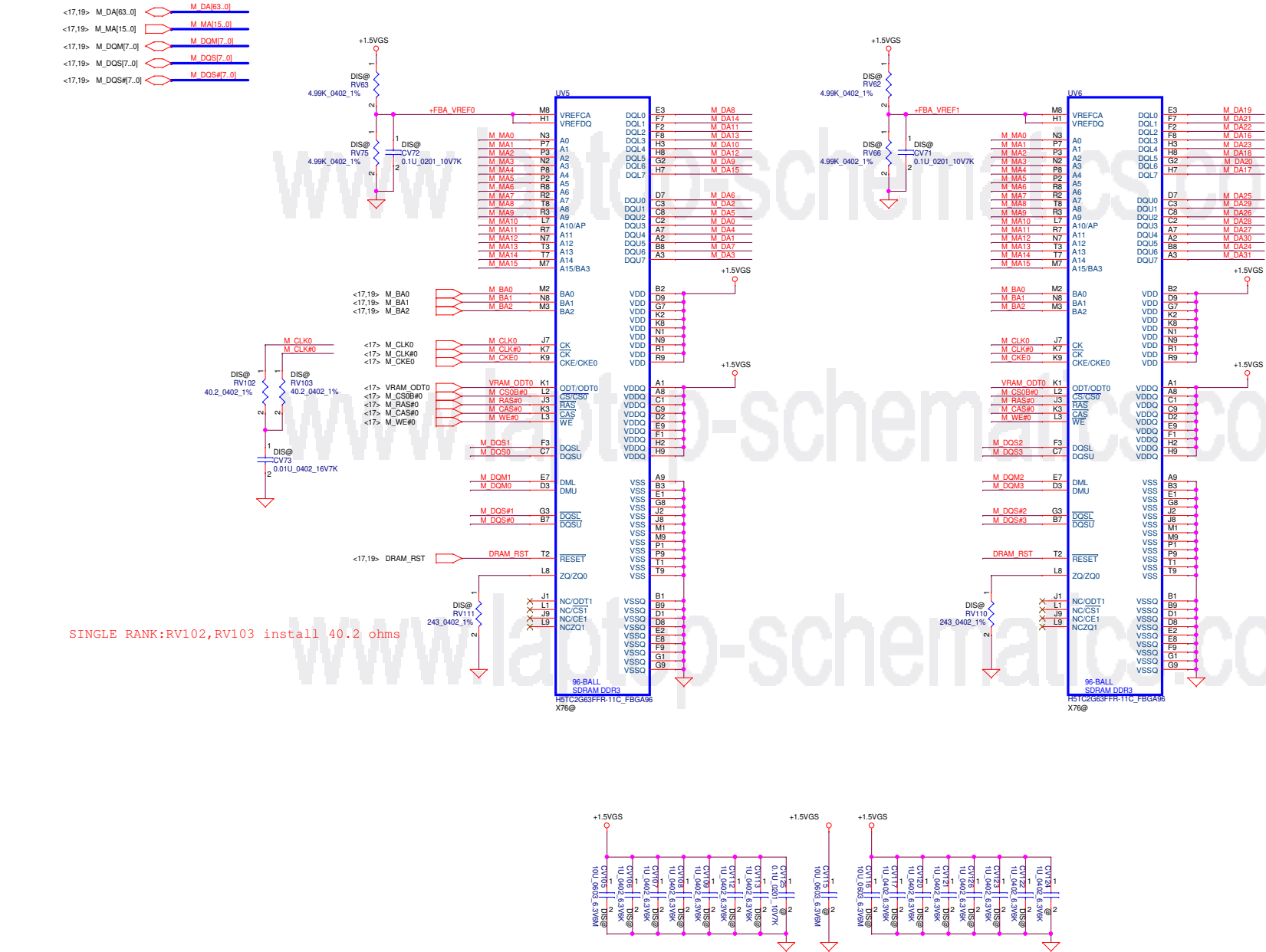


Route 50ohms single-ended/100ohm diff and keep short debug only, for clock observation, if not need, DNI.

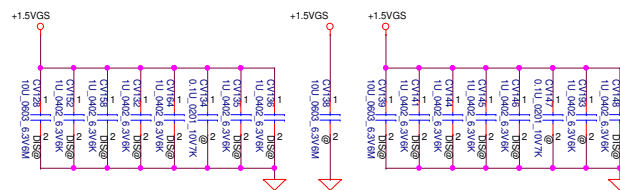
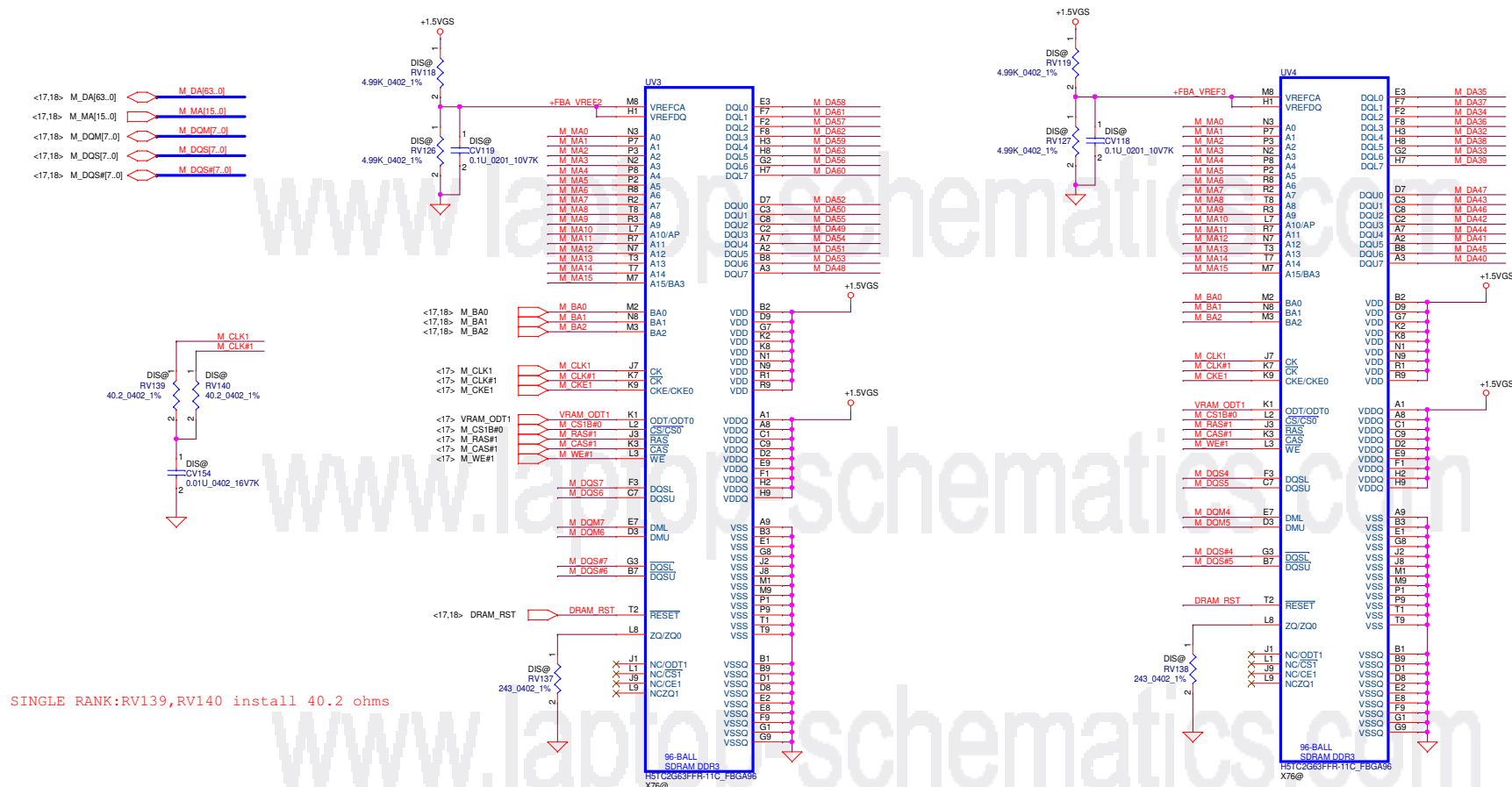


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DDR3 Memory Channel Rank 0:A0

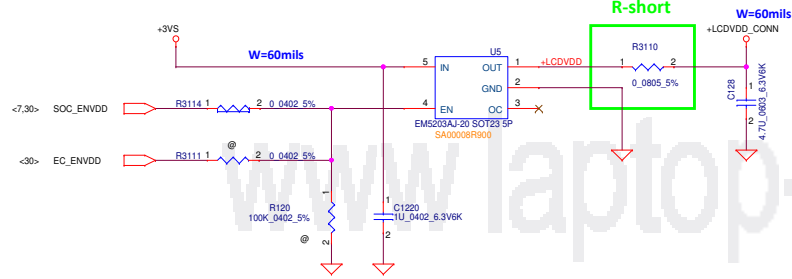


## DDR3 Memory Channel Rank 0:A1

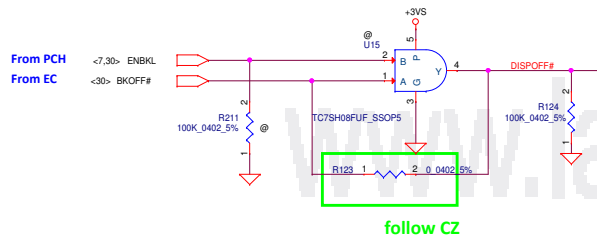
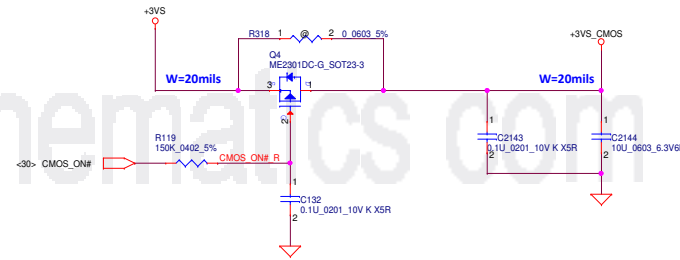


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				Size Custom	Document Number LA-D541P	
Date:		Thursday, December 17, 2015		Sheet	19 of 43	

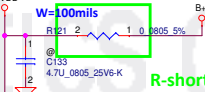
# LCD Power Circuit



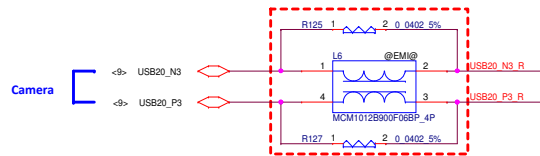
# Camera



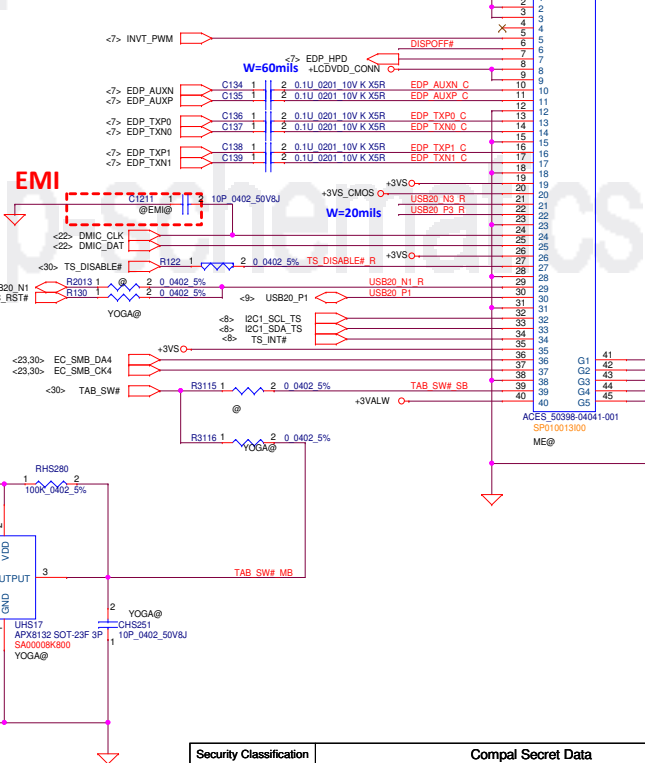
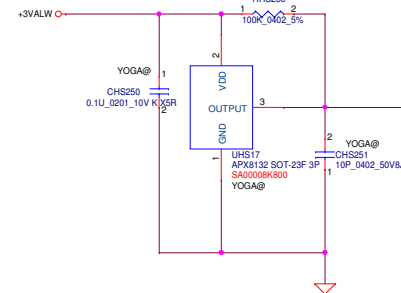
# eDP CONN.



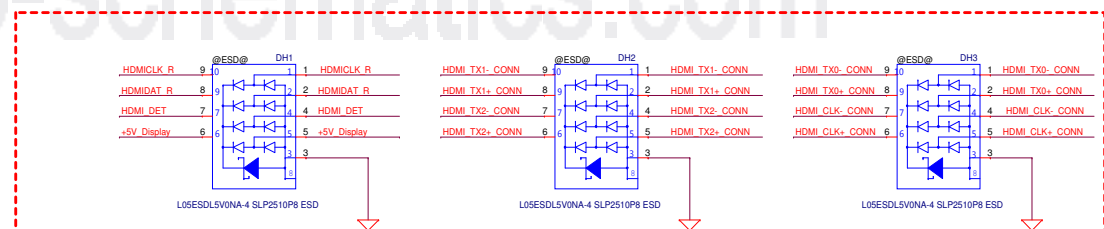
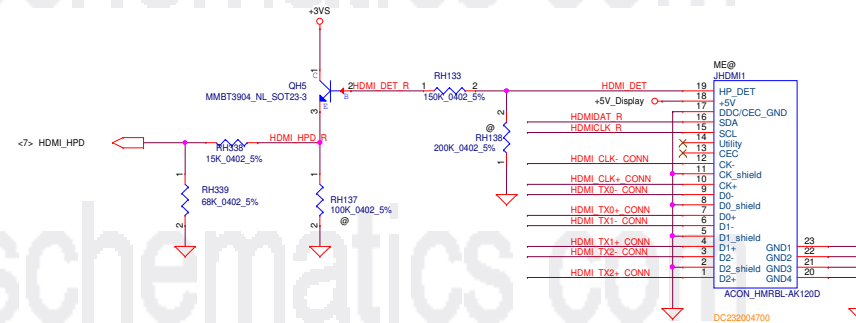
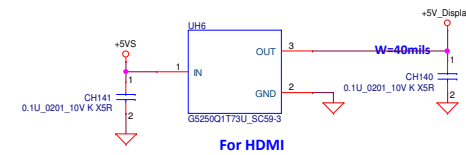
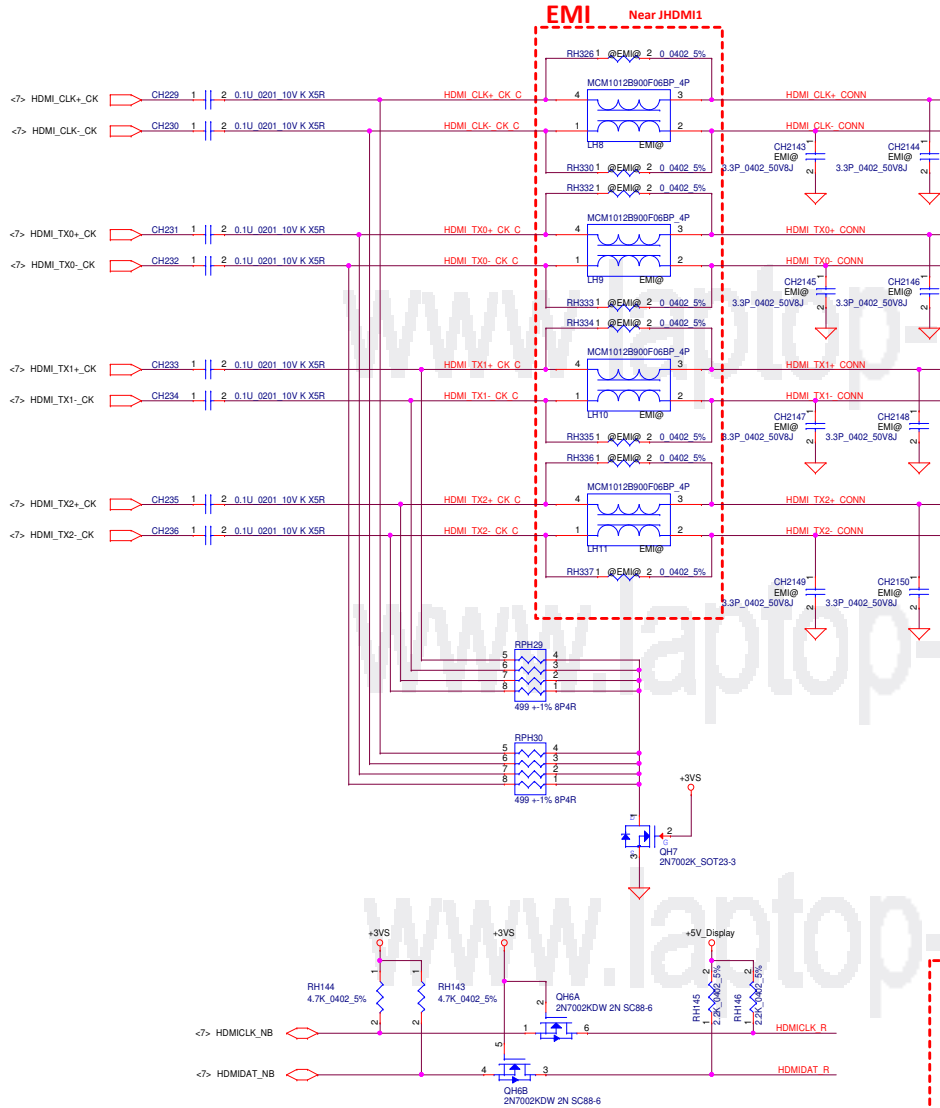
# EMI



# G sensor/Hall sensor

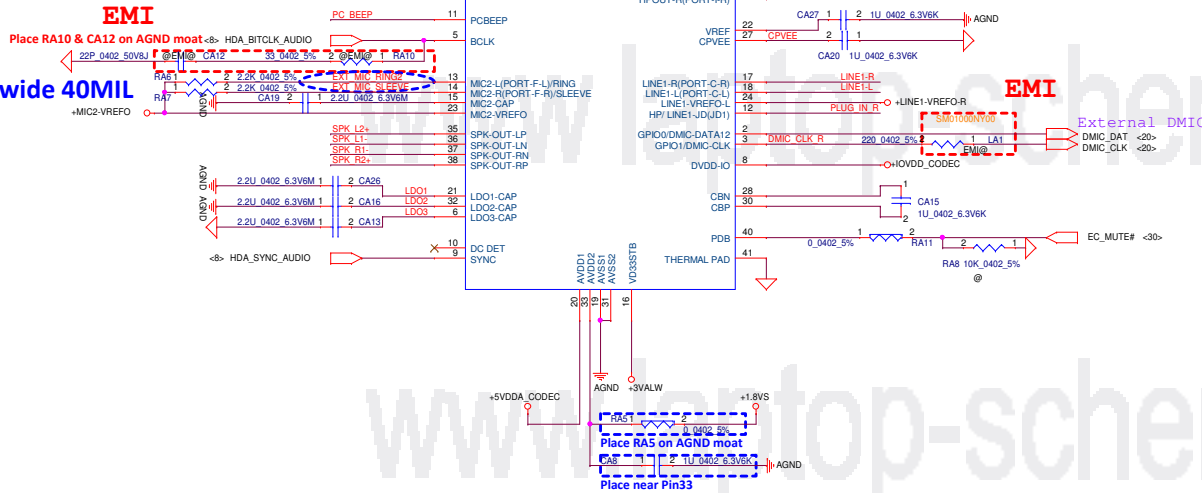


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Size	C	Document Number	LA-D541P	Rev
				0.2
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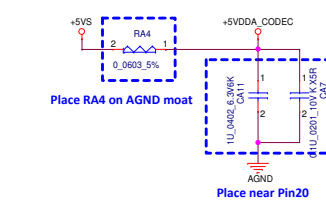


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ALC3240



+5VS → +5VDDA\_CODEC



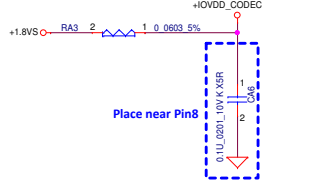
Each Platform Power Net Support List :

	+1.5VS	+1.8VS	+3VS	+5VS	+3VALW
	1.5V(S0)	1.8V(S0)	3.3V(S0)	5V(S0)	3.3V(S0~S5)
Intel Broadwell	V	X	V	V	V
Intel Skylake	X	V	V	V	V

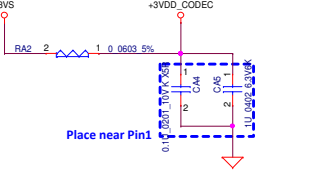
Each Platform HDA Link Voltage Support (Pin 8) :

	3.3V	1.5V
Intel Broadwell	V (default)	V
Intel Skylake	V (default)	V

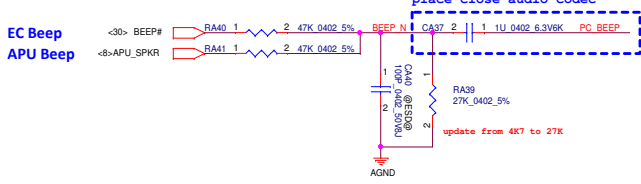
+1.8VS → +IOVDD\_CODEC



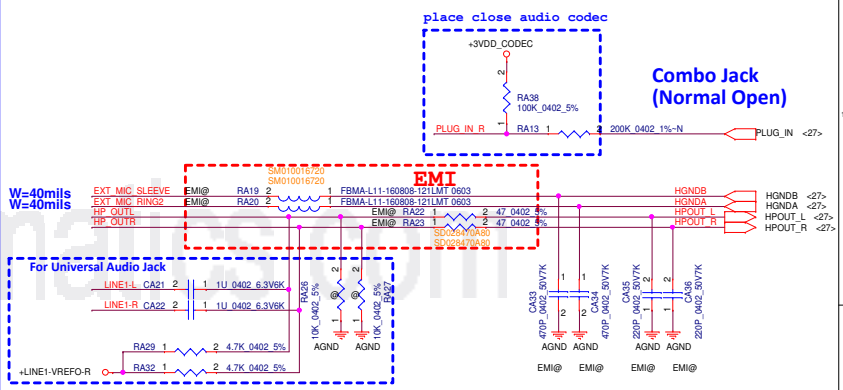
+3VS → +3VDD\_CODEC



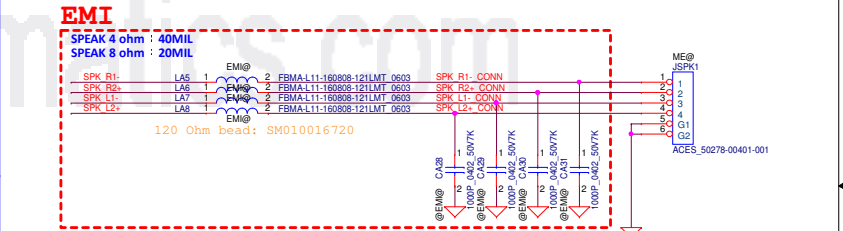
PC BEEP



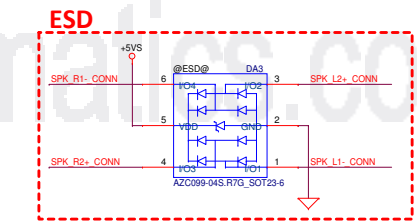
Input



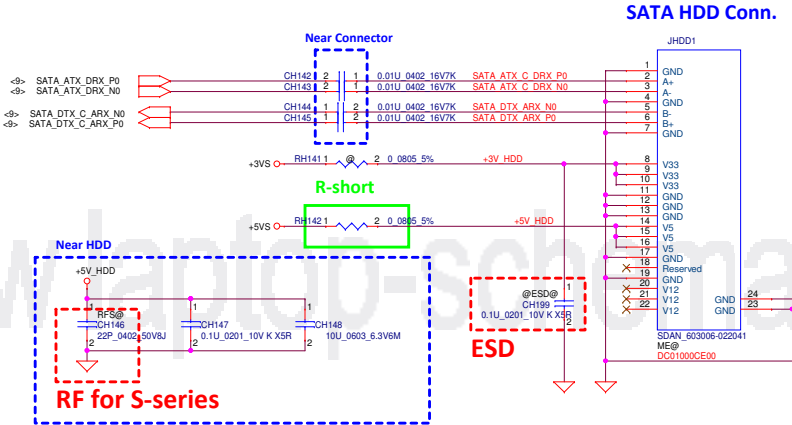
Output



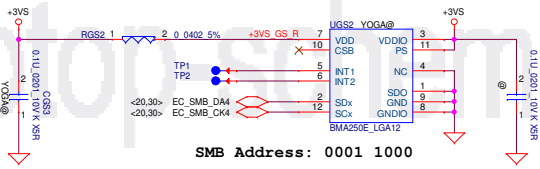
ESD protection needs to be placed near connector side



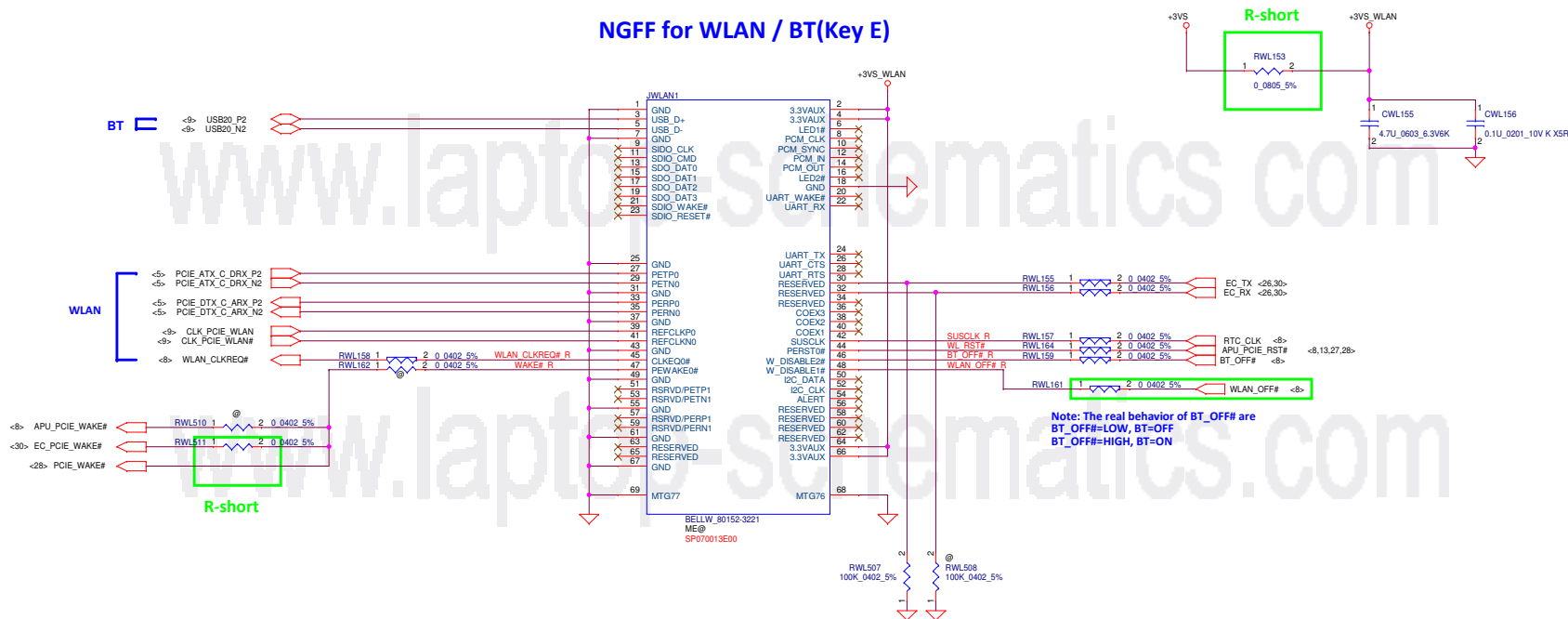
HDD



(G-Sensor for 360-degree reverse)



# NGFF for WLAN / BT(Key E)



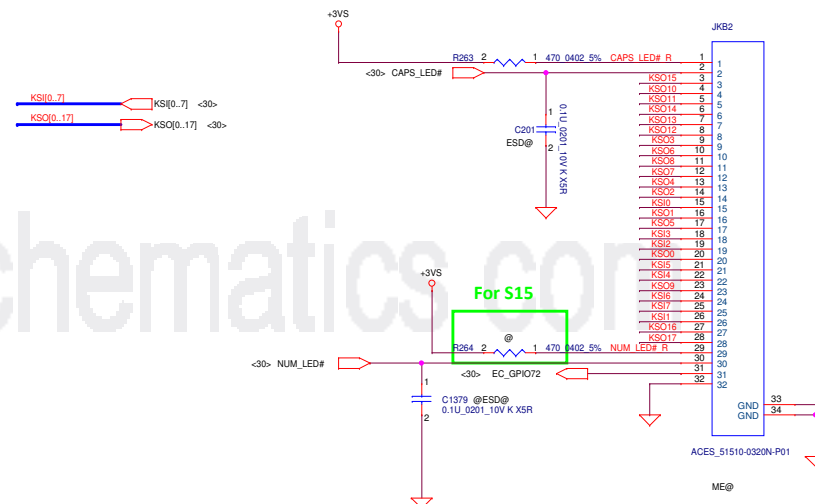
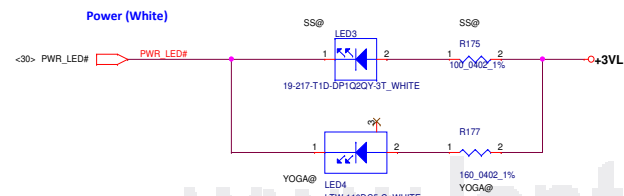
Note: The real behavior of BT\_OFF# are  
BT\_OFF# = LOW, BT-OFF  
BT\_OFF# = HIGH, BT-ON

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Size	Document Number			LA-D541P		Rev 0.2
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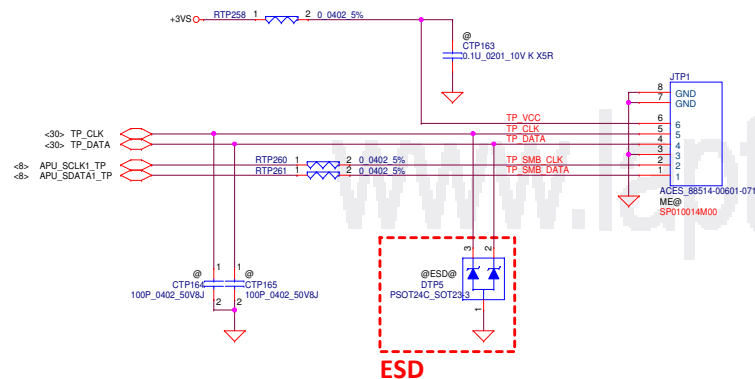




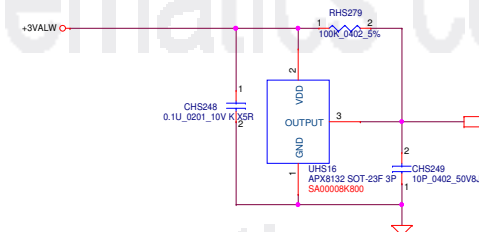
## Keyboard



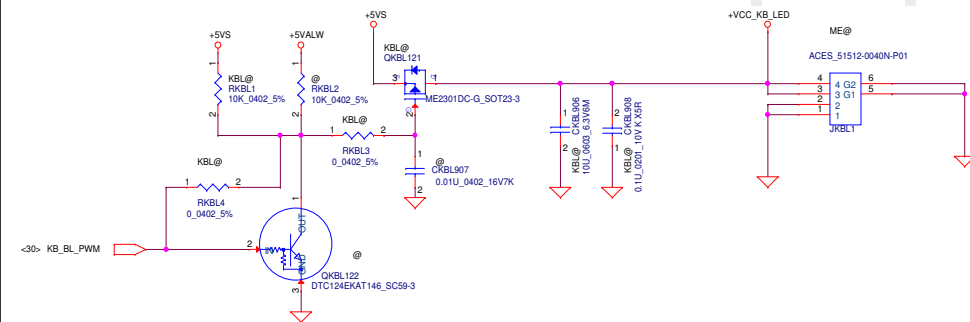
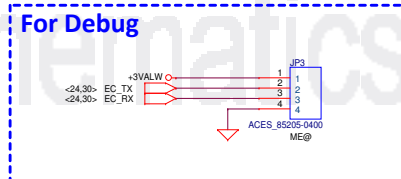
## Touch Pad



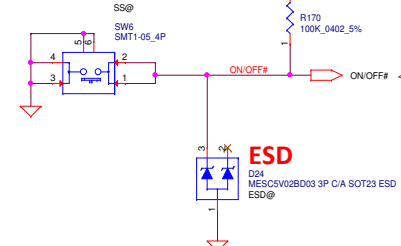
## Hall Sensor & Button



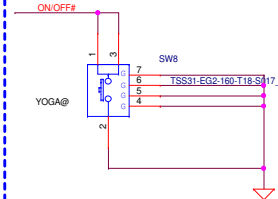
## Keyboard Backlight



## Power Button For S Series

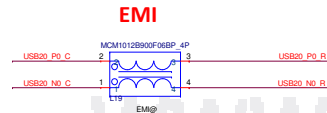
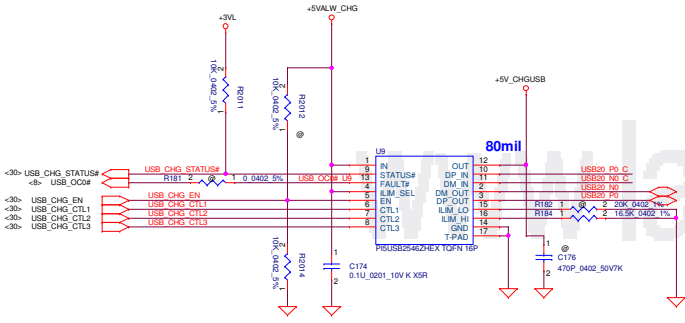


## Power Button For YOGA

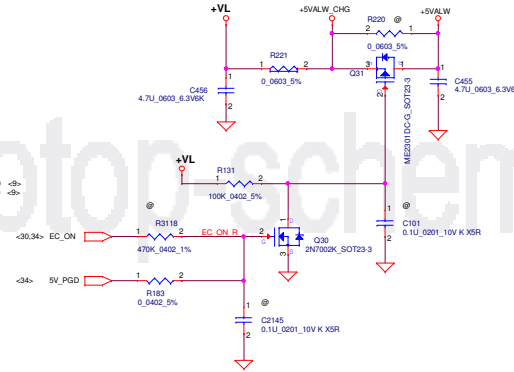


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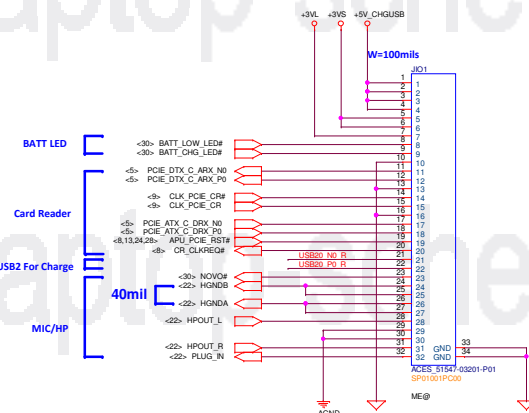
## USB Charge



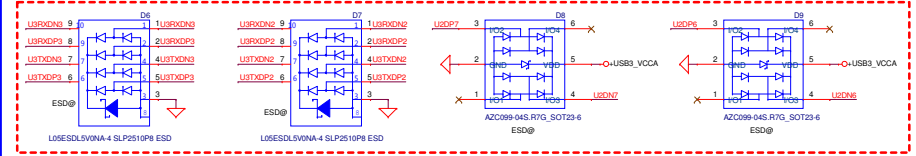
## USB Charge switch



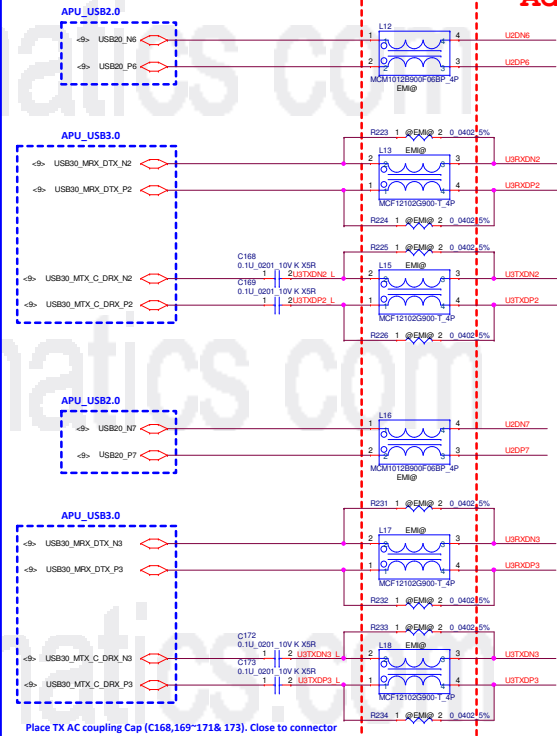
## IO CONN



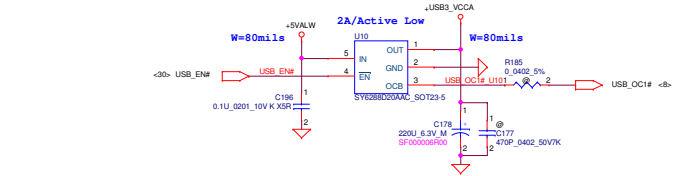
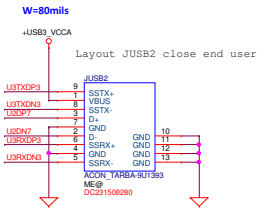
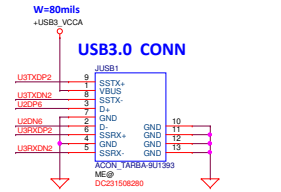
## ESD



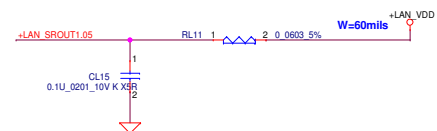
## USB3.0\_Port



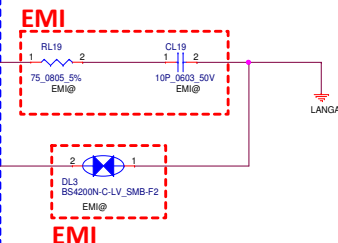
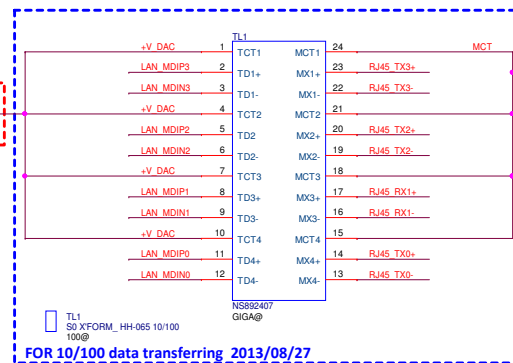
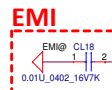
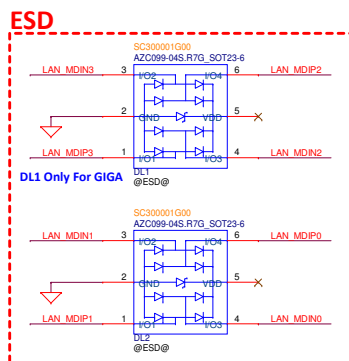
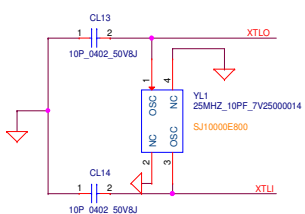
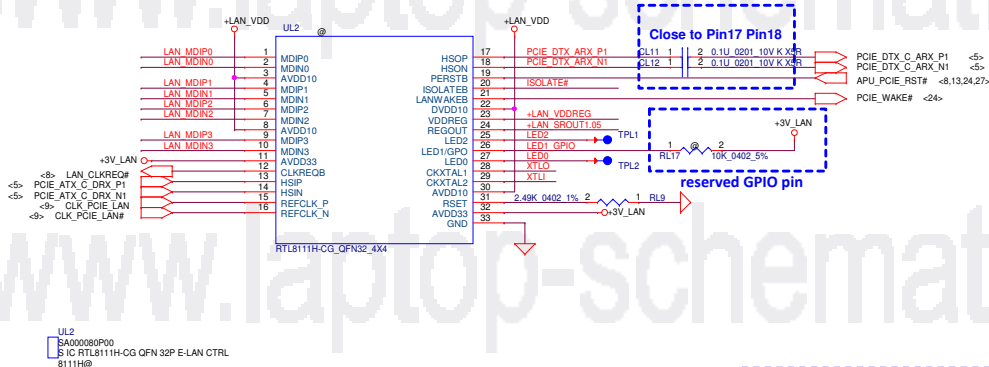
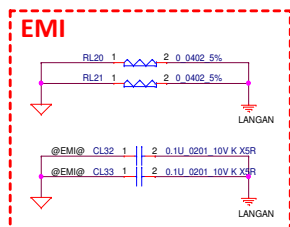
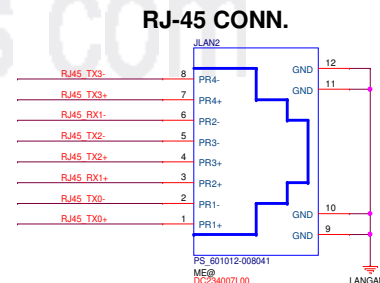
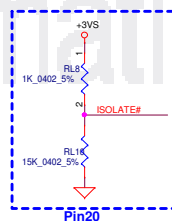
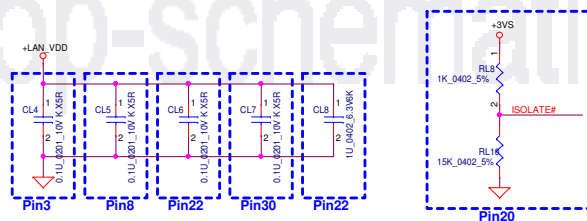
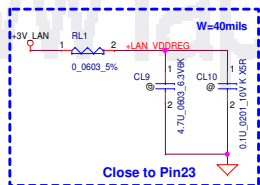
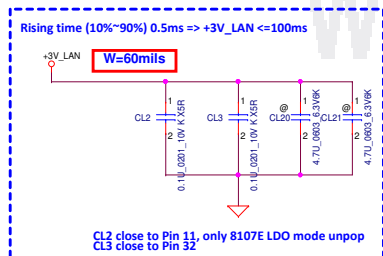
## Add resistor



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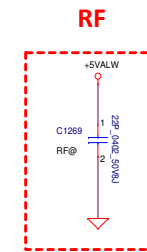
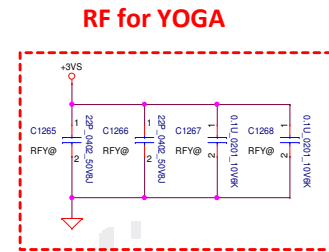
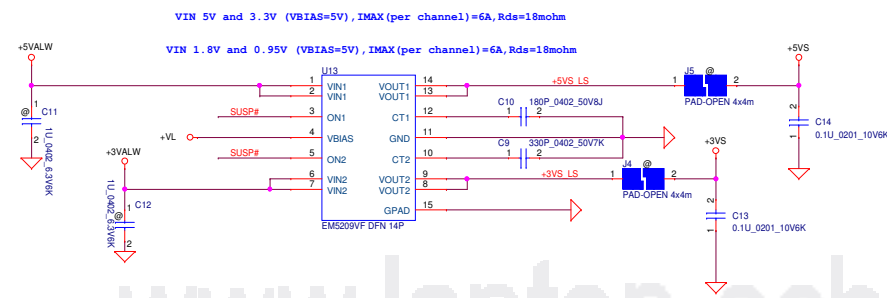


**LL1, CL16, and CL17 close to Pin24  
( Should be place within 200 mils )**

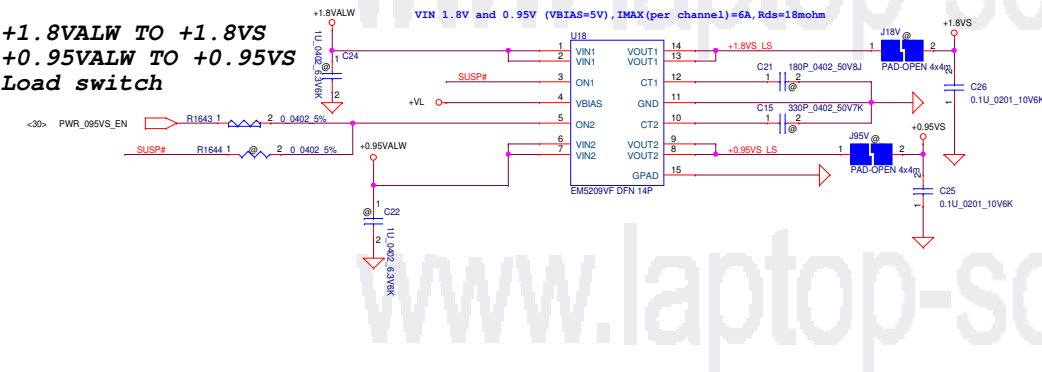


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				Size C	Document Number	Rev 0.
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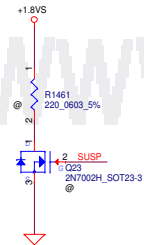
**+5VALW TO +5VS**  
**+3VALW TO +3VS**  
**Load switch**



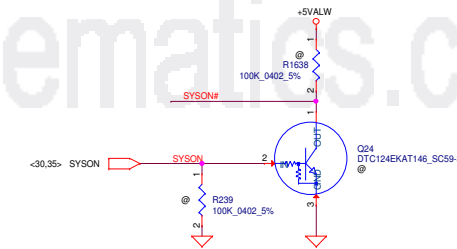
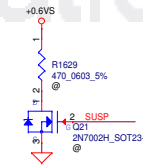
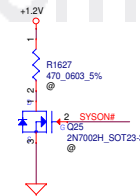
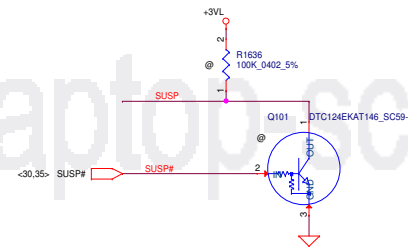
**+1.8VALW TO +1.8VS**  
**+0.95VALW TO +0.95VS**  
**Load switch**



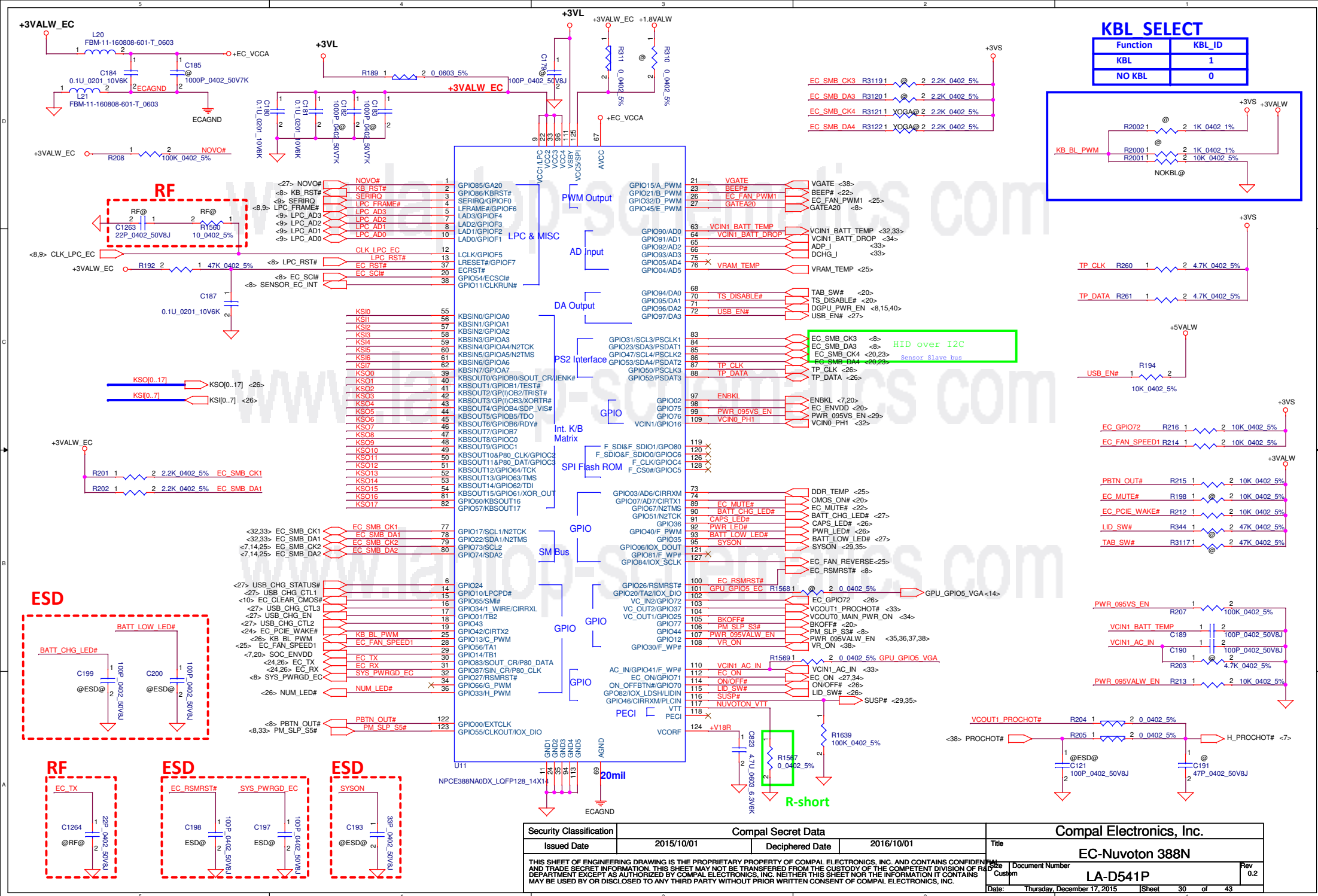
**+1.5VS discharge circuit only for Beema**  
**only 1.5VS from PWR**



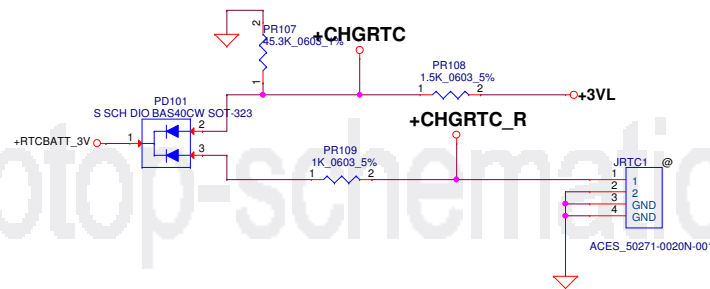
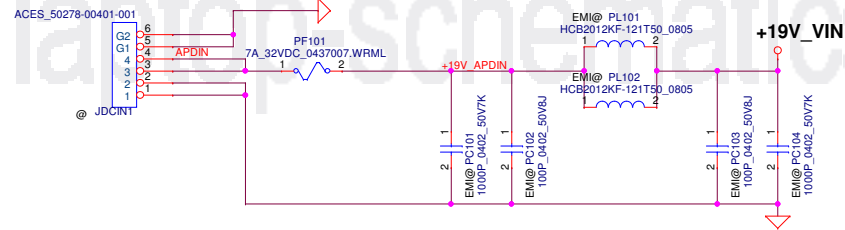
**only for Beema**



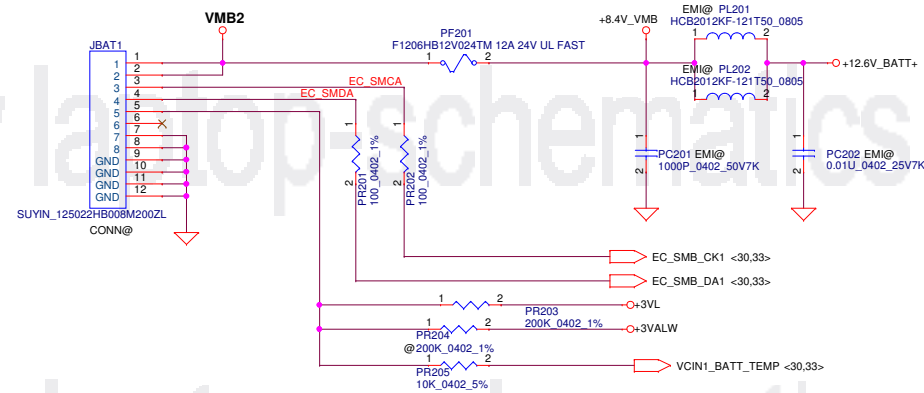
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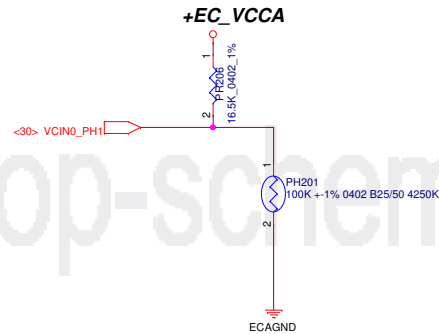
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**PH201 under CPU bottom side :**  
**CPU thermal protection at 93 +-3 degree C**  
**Recovery at 56 +-3 degree C**



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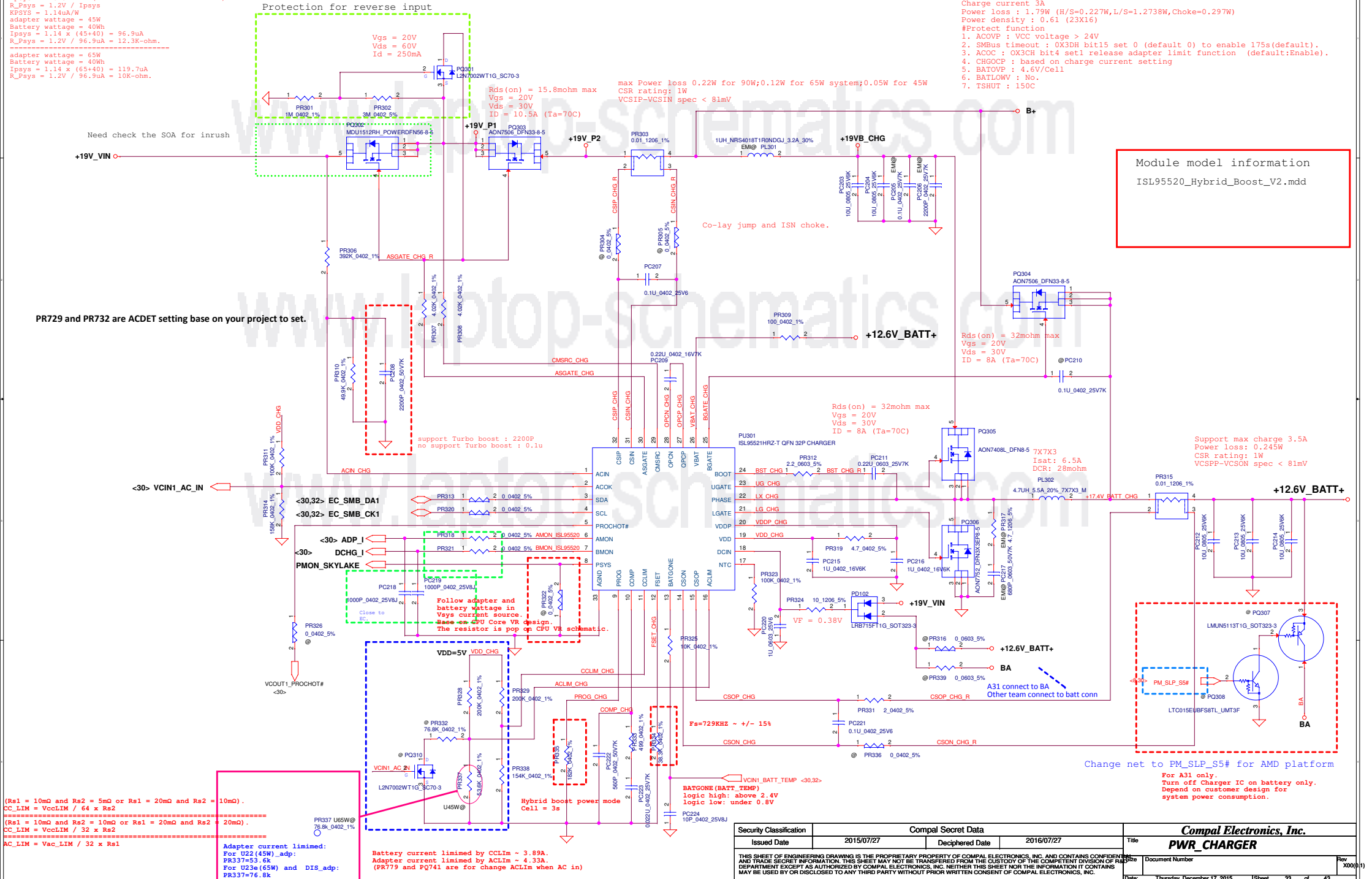
```

Ipsys = KPSYS  x ( VADP x IADP + VBAT x IBAT )
R_Psys = 1.2V / Ipsys
KPSYS = 1.14uA/W
adapter wattage = 45W
Battery wattage = 40Wh
Ipsys = 1.14 (45+40) = 96.9uA
R_Psys = 1.2V / 96.9uA = 12.3K-ohm.
-----
adapter wattage = 65W
Battery wattage = 40Wh
Ipsys = 1.14 x (65+40) = 119.7uA
R_Psys = 1.2V / 96.9uA = 10K-ohm.

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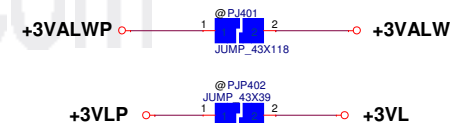
Module model information  
ISL95520 Hybrid Boost V2.mdd

PR729 and PR732 are ACDET setting base on your project to set.

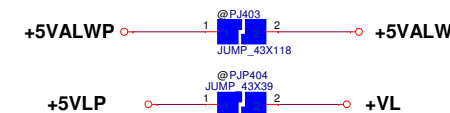


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## SY8286B\_V1.mdd



## SY8286C\_V1.mdd

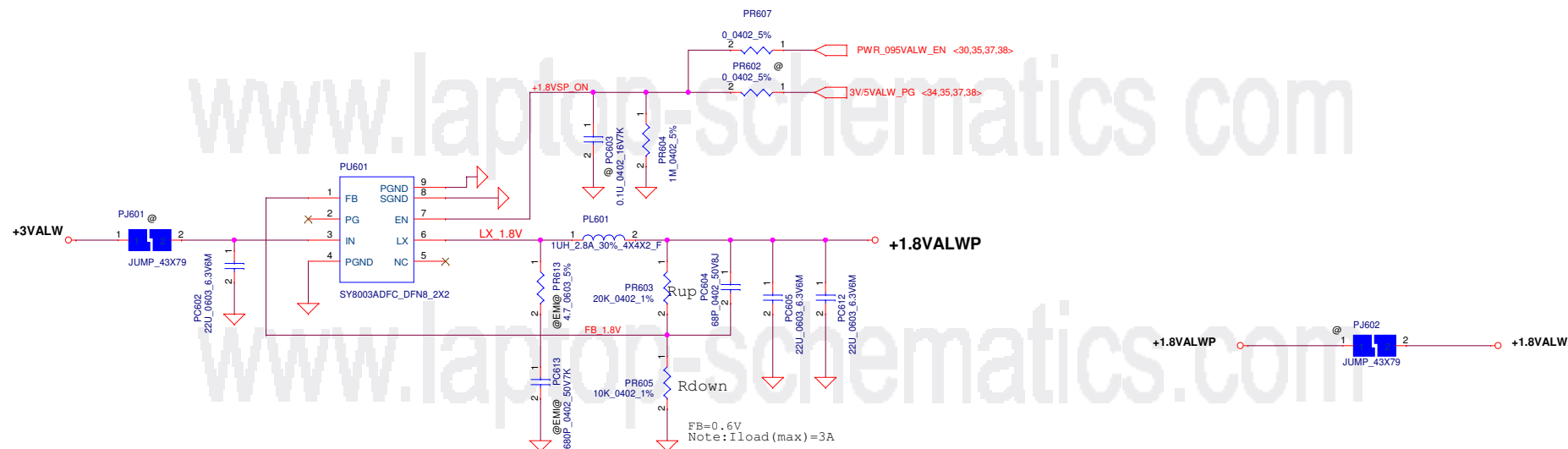


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# Module model information

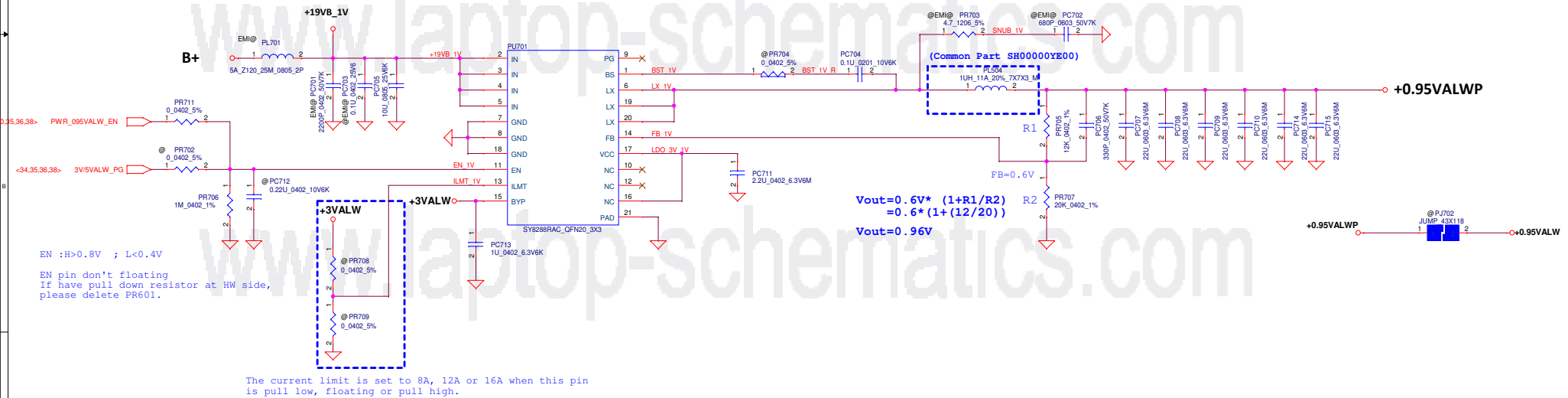
SY8003A\_V1.mdd



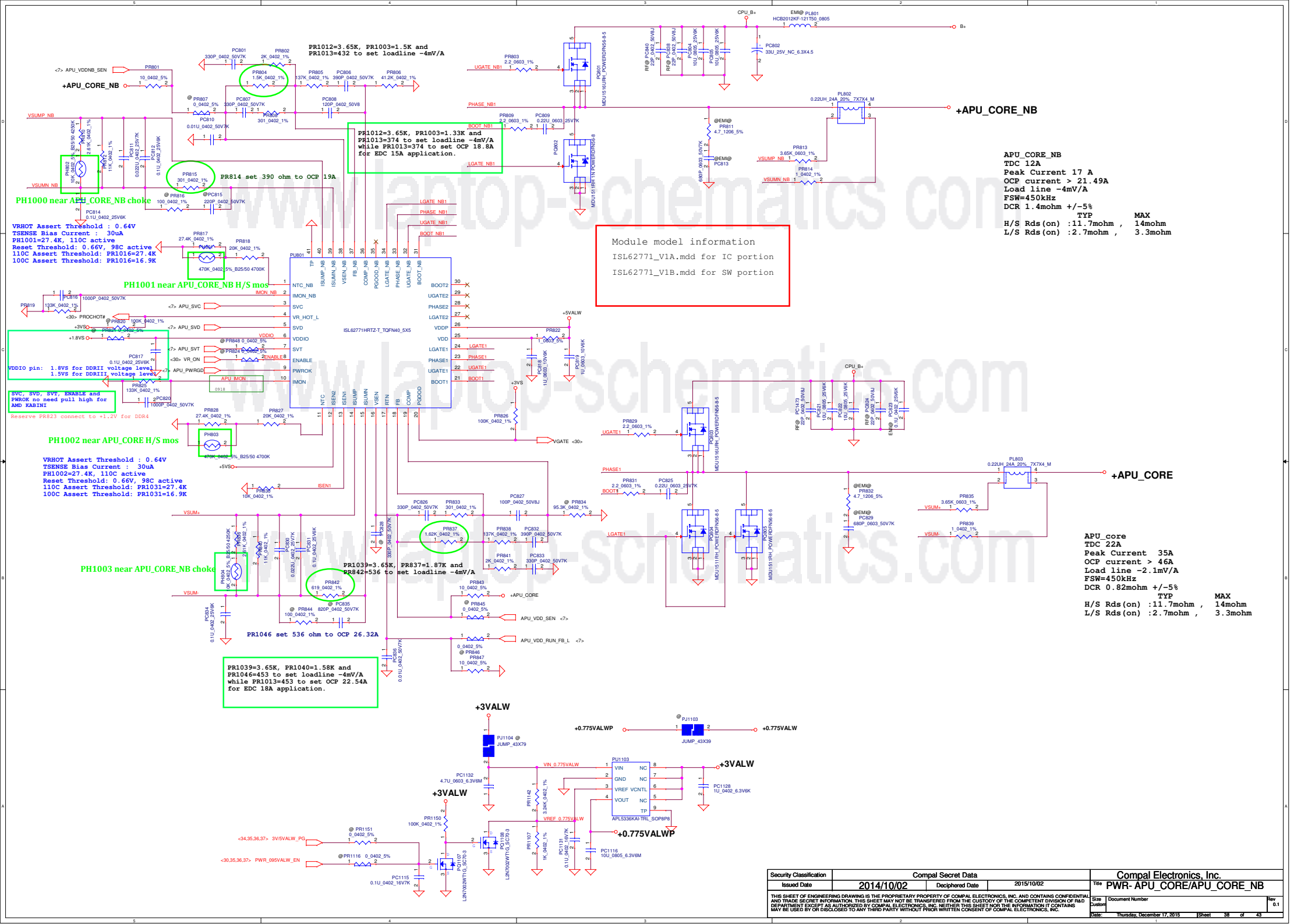
2015/10/28 Delete +1.5VS power rail

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Module model information  
SY8288\_V1.mdd



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PR1012=3.65K, PR1003=1.5K and PR1013=432 to set loadline -4mV/A

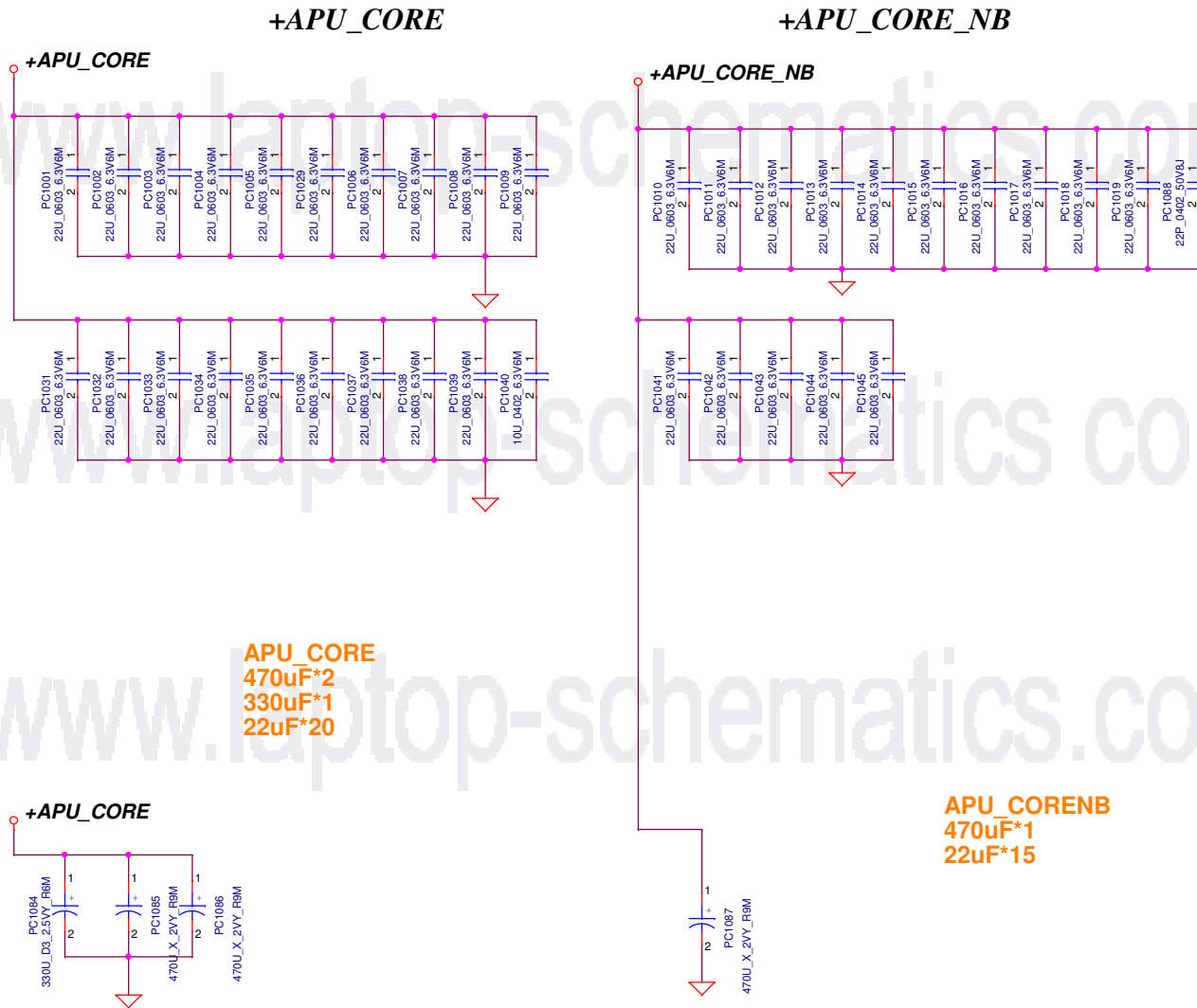
PR1012=3.65K, PR1003=1.33K and PR1013=374 to set loadline -4mV/A while PR1013=374 to set OCP 18.8A for EDC 15A application.

PR1039=3.65K, PR1040=1.58K and PR1046=453 to set loadline -4mV/A while PR1013=453 to set OCP 22.54A for EDC 18A application.

APU\_CORE\_NB  
TDC 12A  
Peak Current 17 A  
OCP current > 21.49A  
Load line -4mV/A  
FSW=450kHz  
DCR 1.4mohm +/-5%  
TYP  
H/S Rds(on) :11.7mohm , MAX  
L/S Rds(on) :2.7mohm , 14mohm

APU\_core  
TDC 22A  
Peak Current 35A  
OCP current > 46A  
Load line -2.1mV/A  
FSW=450kHz  
DCR 0.82mohm +/-5%  
TYP  
H/S Rds(on) :11.7mohm , MAX  
L/S Rds(on) :2.7mohm , 3.3mohm

Module model information  
ISL62771\_V1A.mdd for IC portion  
ISL62771\_V1B.mdd for SW portion



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Module model information  
ISL62771\_C2\_GFX35W\_V1A.mdd for IC portion  
ISL62771\_C2\_GFX35W\_V1B.mdd for SW portion

VGA\_M250=>Link +1.8VGS

VGA\_M130=>Link +3VGS

VR\_ON  
High > 1.6V  
Low < 1V

PH1002 near APU\_CORE H/S mos

VRHOT Assert Threshold : 0.64V  
TSENSE Bias Current : 30uA  
PH1002=27.4K, 110C active  
Reset Threshold: 0.66V, 98C active  
110C Assert Threshold: PR1031=27.4K  
100C Assert Threshold: PR1031=16.9K

PH1003 near GFX\_CORE choke

PR1058=3.65K, PR1040=2.1K and  
PR1046=604 to set loadline -2.1mV/A  
while PR1046=594 to set OCP 57.16A  
For EDC 45A application.

EN pin don't floating  
If have pull down resistor at HW side, pls delete PR102

change PL601  
SM01000C000 to comm  
part SM01000P200

The current limit is set to 8A, 12A or 16A when this pin  
is pull low, floating or pull high

Pin 7 BYP is for CS.  
Common NB can delete +3VALW and PC15

$$V_{out} = 0.6V \cdot (1 + R_{up}/R_{down})$$
$$= 0.6V \cdot (1 + (30.1/20))$$
$$V_{out} = 1.503V$$

+VGA\_CORE

GFX\_core  
TDC 30 (1H1L)  
Peak Current > 45A  
OCP current > 45A  
Load line -2.1mV/A  
FSW=400kHz  
DCR 1.19mohm +/-5%  
H/S Rds(on) : 11.7mohm , 14mohm  
L/S Rds(on) : 2.7mohm , 3.3mohm

+VGA\_CORE

SH000010N00 (DCR:1.19mohm +/-5%)

+VGA\_CORE

+VGA\_CORE

+1.5VGS

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## Version change list (P.I.R. List)

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Item	Reason for change	PG#	Modify List	Date	Phase
1	Modify +1.2V OCP setting	35	Modify PR502 from 13K to 6.04K_0402_1%(SD034604180)	2015/11/03	EVT
2	Modify 0.95V feedback resistor	37	Modify PR705 from 14K to 12K_0402_1%(SD034120280)	2015/11/03	EVT
3	Modify CHARGER COMP	33	Modify PC223 from 0.015U to 0.022U_0402_25V7K(SE075223K80)	2015/11/03	EVT
4	Modify CPU_CORE COMP_NB	38	Modify PR806 from 11.5K to 41.2K_0402_1%(SD000009K00)	2015/11/03	EVT
5	Modify CPU_CORE COMP	38	Modify PC827 from 68P to 100P_0402_50V8J(SE071101J80) Modify PR834 to un-mount	2015/11/03	EVT
6	Modify VGA_CORE ISUMN	40	Modify PR1427 from 604 to 549_0402_1%(SD034549080)	2015/11/03	EVT
7	Modify VGA_CORE Rdroop	40	Modify PR1424 from 2.1K to 1.37K_0402_1%(SD034137180)	2015/11/04	EVT
8	Modify +0.95VALW output capacitor	37	Add PC714 & PC715 (22U_0603_6.3V6M)	2015/11/04	EVT
9	Modify capacitor size	38	PC812 & PC814 & PC831 & PC834 change from 0603 to 0.1U_0402_25V6K (SE00000G880)	2015/11/06	EVT
10	Modify 0 ohm P/N	35	PR511 change from to 0_0402_1% to 0_0402_5%(SD028000080)	2015/11/09	EVT
11	Modify APU CORE IC VDDIO for DDR4 design	38	Change PR821 to 0_0402_5%(SD028000080) Reserve PR823 connect to +1.2V for DDR4	2015/11/10	EVT
12	PSYS add 0ohm connect to GND for AMD platform	33	PR322 change to 0_0402_5%((SD028000080))	2015/11/12	EVT
13	Change VGA CHOKE for thermal team request	40	Change PL1402 & PL1403 from 0.24UH_22A_+-20%_7X7X3_M(SH000010N00) to 0.22UH_24A_20%_7X7X4_M(SH000011H00)	2015/11/12	EVT
14	Modify DGPU_PWR_EN sequence for GPU CORE IC	40	Add PC1472 (0.22U_0402_10V6K, SE095224K00) Change PR1444 from 0_0402_5%(SD028000080) to 100K_0402_5%(SD028100380)	2015/11/12	EVT
15	Modify VGA CHOKE location	40	Change location from PL1406 to PL1403	2015/11/18	EVT
16	Add EMI solution		1.PJ301 change to PL301 ISN CHOKE (SH00000YG00) 2.PJ701 change to PL701 EMI bead (SM01000P200) 3.PR312 change from 0_0603_5% to 2.2_0603_5% 4.Add PR317 , PC217 , PC205	2015/12/03	DVT
17	To avoid VL protection for HW USB CHG	34	1.Add PC431(1U_0402_16V6K , SE000000U00) 2.Add PR415(100K_0402_5% , SD028100380)	2015/12/14	DVT
18	Add RF solution for CPU CORE	38	PR803 & PR829 change from 0 ohm to 2.2 0hm	2015/12/14	DVT
19	Change APU_CORE output capacitor	39	PC1084 change from 470U_X_2VY_R9M to 330U_D3_2.5VY_R6M(SGA00006A00)	2015/12/14	DVT
20	Add RF solution		Add PC432 , PC838 , PC839 , PC1088 (22P_0402_50V8J, SE068220K80)	2015/12/14	DVT

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Item	Reason for change	PG#	Modify List	Date	Phase
1	Modify footprint for QFN IC		Modify PU301,PU401,PU402,PU501,PU701,PU801,PU1401,PU1402 footprint	2015/12/16	DVT
2	ADD RF solution	38	Add PC840(22P_0402_50V8J,SE068220K80)	2015/12/16	DVT
3	To reduce 0-ohm part count		Change PR304,PR305 ,PR336,PR322,PR821,PR1410 to 0-ohm short pad	2015/12/16	DVT
4	Delete EVT reserve 0-ohm part		Delete PR823 & PR1408(EVT reserve only)	2015/12/16	DVT

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